



PRODUCT SPECIFICATION

PART NUMBER # REV: FLC-150GML6000SA1#00

DESCRIPTION: 15" TFT 1024x768 TFT Full View 1500CD with LVDS interface

() Preliminary Specification

(V) Approved Specification

Customer Name:	
Signature:	Date:

PREPARED BY	REVIEWED BY
<i>Mia</i>	<i>David</i>

Revision History

Version	Date	Page	Description	Note
V1.0	2020/10/16		1 st Edition	
V1.1	2020/10/19		Revised Information	
V1.2	2020/10/21		Update LED connector info	P8
V1.3	2021/02/15		Update info	

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Table of Content

1.1 Description4
 1.2 Product Summary4
2. ABSOLUTE MAXIMUM RATING.....5
 2.1 Electrical Absolute Rating5
 2.2 Environment Absolute Rating.....5
3. ELECTRICAL CHARACTERISTICS.....6
 3.1 LCM Power Specification6
 3.2 Backlight Unit8
 3.3 COLOR DATA INPUT ASSIGNMENT10
4. SIGNAL CHARACTERISTICS.....11
 4.1 Interface Timing11
 4.1.1 Timing Characteristics:11
 4.1.2 Power ON/OFF Sequence14
 4.1.3 SCANNING DIRECTION15
5. INTERFACE PIN DESCRIPTION16
 5.1 LCM Connector PIN Assignment16
6. BLOCK DIAGRAM.....18
7. OPTICAL CHARACTERISTIC.....19
 8. DIMENSION AND DRAWING Errore. Il segnalibro non è definito.
9. PRECAUTION AND PRODUCT HANDLING22

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1. GENERAL DESCRIPTION

1.1 Description

15" is a Color Active Matrix Liquid Crystal Display Module composed of a TFT LCD panel and LED backlight system. The screen format is intended to support FHD, 1024x768 screen and 16.2M/262K colors.

1.2 Product Summary

The following items are summary on the table under Ta=25 °C condition:

No.	Item	Specification	Unit
1	Display Size	15"	Inch
2	Pixel Number	1024 (H) x RGB x 768 (V)	Pixels
3	Outline Dimension	326.5 (H) × 253.5(V) × 9.1(D)	mm
4	Active Area	304.1 (H) × 228.1 (V)	mm
5	Display Colors	16.7M/ 262K	--
6	Pixel Arrangement	RGB vertical stripe	--
7	Display Mode	Normally Black	--
8	Electrical Interface	LVDS	
9	Surface Treatment	Hard coating 3H, Anti-Glare	--
10	Brightness	1,500 (Typ.)	cd/m2
11	Contrast Ratio	2,500 (Typ.)	--
12	Total Power Consumption (Typ)	22.28	W

2. ABSOLUTE MAXIMUM RATING

2.1 Electrical Absolute Rating

TFT-LCD

Item	Symbol	Min	Max.	Unit	Note
Power Supply Voltage	VCC	-0.3	4	V	(1)

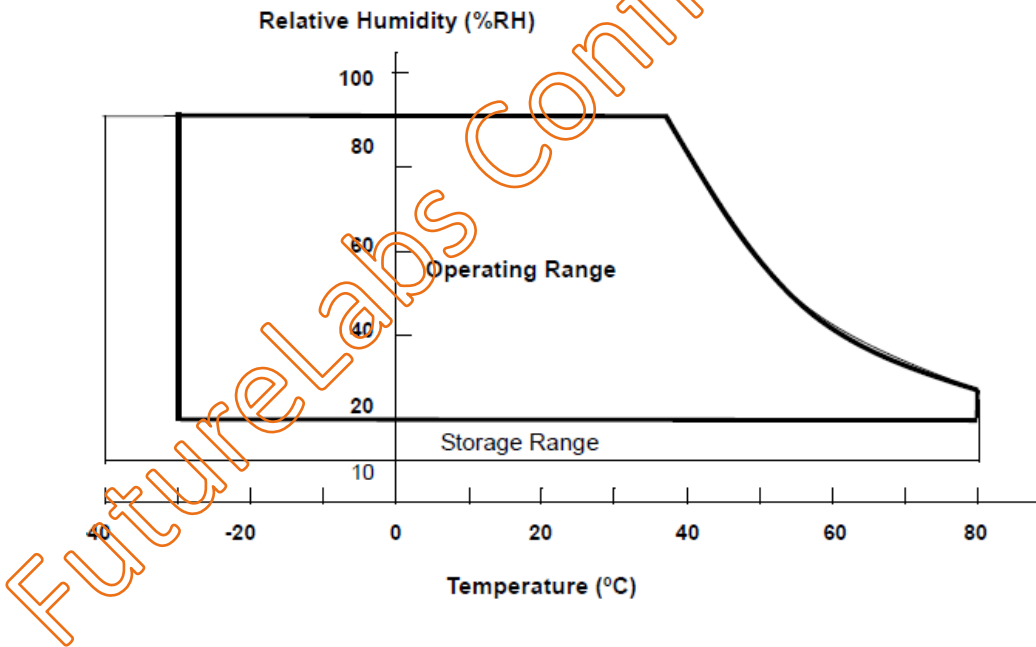
2.2 Environment Absolute Rating

Item	Symbol	Min	Max	Unit	Note
Operating Temperature	Top	-30	+80 (Panel Surface)	°C	(1)(2)(3)
Storage Temperature	T _{ST}	-40	+80	°C	

Note (1) Temperature and relative humidity range is shown in the figure below.

Note (2) 90 %RH Max. (Ta < 40°C).

Note (3) Wet-bulb temperature should be 39°C Max.



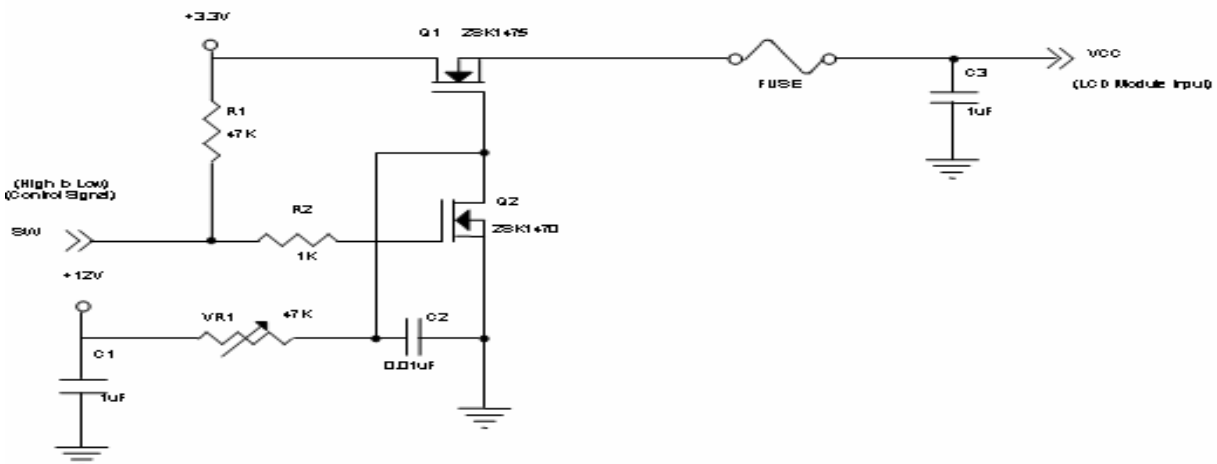
3. ELECTRICAL CHARACTERISTICS

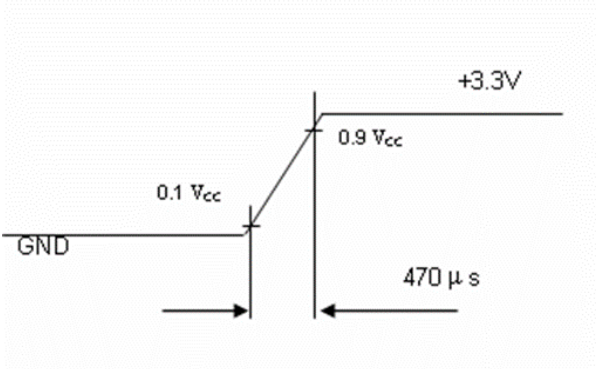
3.1 LCM Power Specification

Parameter	Symbol	Value			Unit	Note
		Min.	Typ.	Max.		
Power Supply Voltage	VCC	3.0	3.3	3.6	V	
Ripple Voltage	VRP			100	mVp-p	
Rush Current	I _{RUSH}			2.0	A	(2)
Power Supply Current White patten	I _{CC}		800	960	mA	(3)a
Power Supply Current Black patten	I _{CC}		670	800	mA	(3)b
LVDS differential input voltage	V _{id}	200		600	mV	
LVDS common input voltage	V _{IC}	1.0	1.2	1.4	V	
Differential Input Voltage for LVDS Receiver Threshold "H" Level	V _{IH}			100	mV	
Differential Input Voltage for LVDS Receiver Threshold "L" Level	V _{IL}	-100			mV	
Terminating Resistor	RT		100		ohm	

Note (1) The module *should* be always operated within above ranges.

Note (2) Measurement Conditions





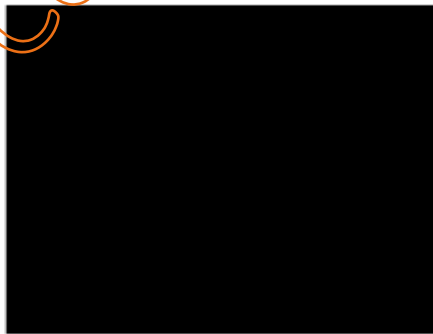
Note (3) The specified power supply current is under the conditions at $V_{DD}=3.3V$, $T_a = 25 \pm 2 \text{ }^\circ\text{C}$, DC Current and $f_v = 60 \text{ Hz}$, whereas a power dissipation check pattern below is displayed.

a. White Pattern



Active Area

b. Black Pattern



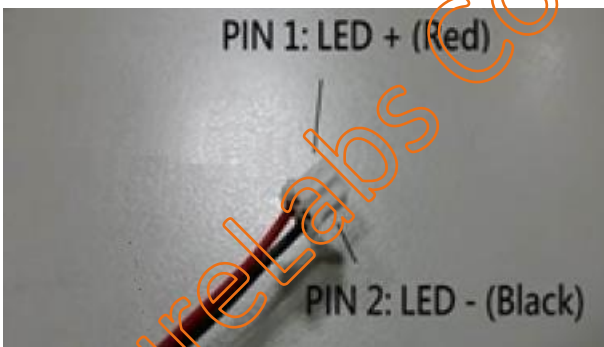
Active Area

3.2 Backlight Unit

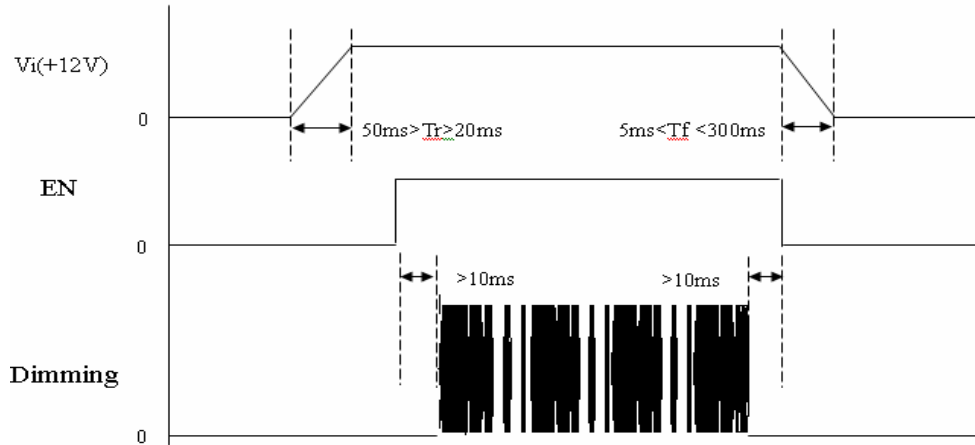
Parameter	Symbol	Min	Typ	Max	Units	Note
LED Light Bar Forward Voltage	V_i	17	-	19	V	
LED Light Bar Forward Current	I_i	0.95	0.96	0.97	A	(Duty 100%)
Backlight Power Consumption	PBL	-	17.82	-	W	(Duty 100%)
LED Life Time	LL	50,000	-	-	Hrs	(1)

Note (1) The lifetime of LED is estimated data and defined as the time when it continues to operate under the conditions at $T_a = 25 \pm 2 \text{ }^\circ\text{C}$ and Duty 100% until the brightness becomes $\leq 50\%$ of its original value. Operating LED under high temperature environment will reduce life time and lead to color shift.

LED Connector: JST PHR-2 or Compatible

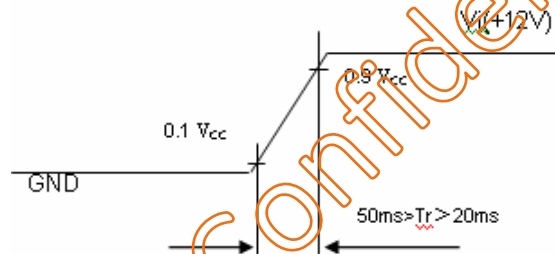


Power sequence and control signal timing are shown in the following figure



Note(2) : While system is turned ON or OFF, the power sequences must follow as below descriptions

Turn ON sequence: Vi(+12V) → EN → Dimming Turn OFF sequence: Dimming → EN → Vi(+12V)



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3.3 COLOR DATA INPUT ASSIGNMENT

The brightness of each primary color (red, green and blue) is based on the 8-bit gray scale data input for the color. The higher the binary input the brighter the color. The table below provides the assignment of color versus data input.

Color		Data Signal																							
		Red								Green								Blue							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale Of Red	Red(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

	Red(252)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(252)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(252)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(252)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale Of Green	Green(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0

	Green(252)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	Green(252)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green(252)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Green(252)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Gray Scale Of Blue	Blue(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0

	Blue(252)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	1
	Blue(252)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0
	Blue(252)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1
	Blue(252)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1

Note (1)0: Low Level Voltage, 1: High Level Voltage

4. SIGNAL CHARACTERISTICS

4.1 Interface Timing

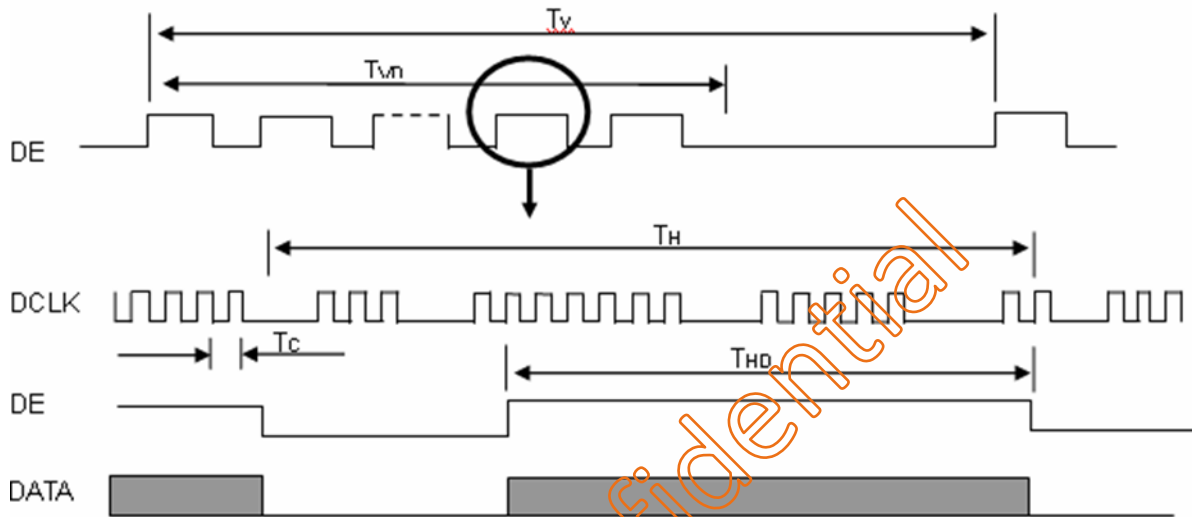
4.1.1 Timing Characteristics:

Parameter	Item	Symbol	Min.	Typ.	Max.	Unit	Note
LVDS Clock	Frequency	Fc	53.35	65	80	MHz	
	Period	Tc	12.5	15.38	18.75	ns	
	Input cycle to cycle jitter	Trcl	--	--	200	ns	(a)
	Input Clock to data skew	TLVCCS	-0.02*Tc	-	0.02*Tc	ps	(b)
	Spread spectrum modulation range	Fclkin_mod	-	-	1.02*Fc	MHz	(c)
	Spread spectrum modulation frequency	Fssm			200	KHz	
Vertical Display Term	FrameRate	Fr	55	60	70	Hz	Tv=Tvd+Tvb
	Total	Tv	780	806	840	Th	
	Active Display	Tvd	768	768	768	Th	
	Blank	Tvb	Tv-Tvd	38	Tv-Tvd	Th	
Horizontal Display Term	Total	Th	1240	1344	1360	Tc	Th=Thd+Thb
	Active Display	Thd	1024	1024	1024	Tc	
	Blank	Thb	Th-Thd	320	Th-Thd	Tc	

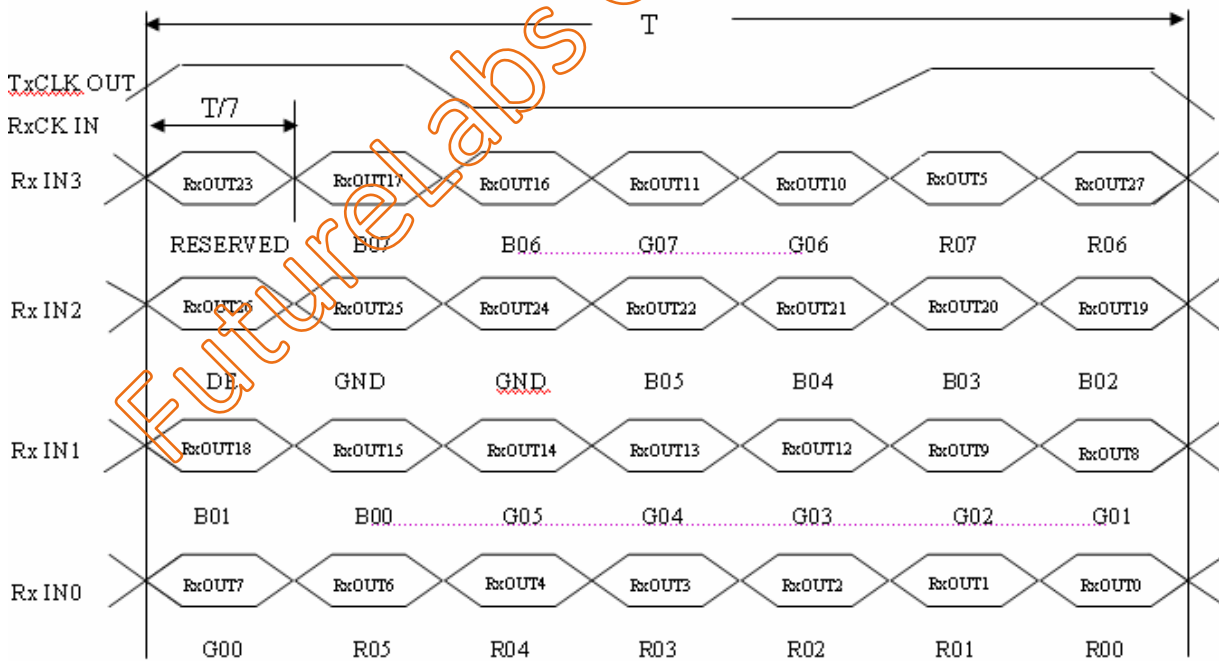
Note (1) DE Mode only, Hsync and Vsync input signals should be set to low logic level or ground. Otherwise, this module would operate abnormally

(2) The Tv(Tvd+Tvb) must be integer, otherwise, the module would operate abnormally

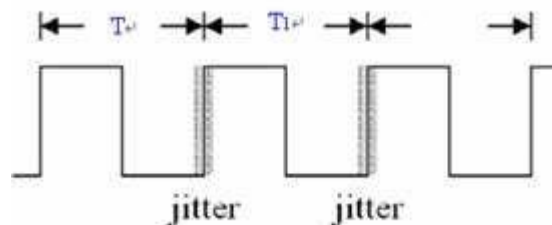
INPUT SIGNAL TIMING DIAGRAM



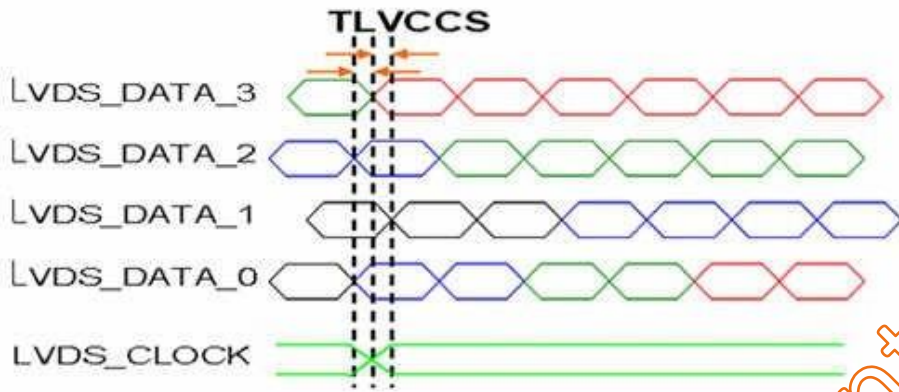
TIMING DIAGRAM of LVDS



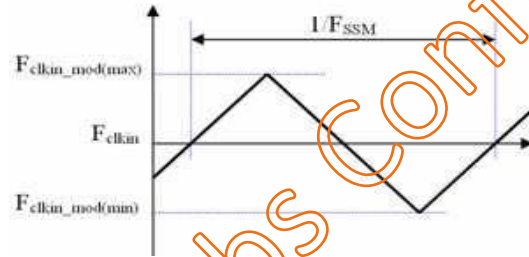
Note (a) The input clock cycle-to-cycle jitter is defined as below figures. $Trcl = |T1 - T1|$



Note (b) Ir



Note (c) The SSCG (Spread spectrum clock generator) is defined as below figures.

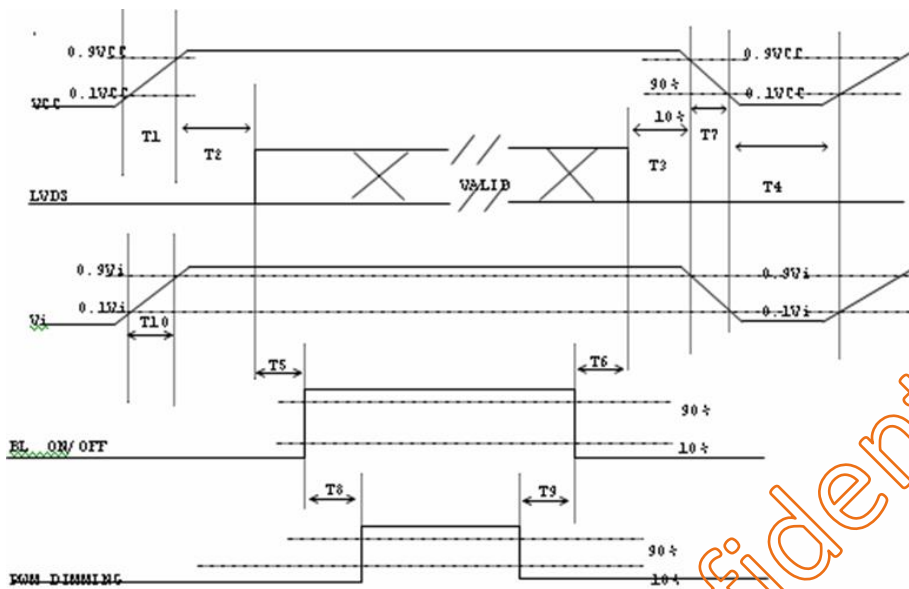


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4.1.2 Power ON/OFF Sequence

To prevent a latch-up or DC operation of LCD assembly, the power on/off sequence should be as the diagram below.

Power ON/OFF sequence



Parameter	Value			Unit
	Min	Typ	Max	
T1	0.5	-	10	ms
T2	0	-	50	ms
T3	0	-	50	ms
T4	500	-	-	ms
T5	200	-	-	ms
T6	200	-	-	ms
T7	5	-	300	ms
T8	10	-	-	ms
T9	10	-	-	ms
T10	20	-	50	ms

Note (1) Please avoid floating state of interface signal at invalid period.

Note (2) When the interface signal is invalid, be sure to pull down the power supply of LCD VCC to 0 V.

Note (3) The Backlight converter power must be turned on after the power supply for the logic and the interface signal is valid.

4.1.3 SCANNING DIRECTION

The following figures show the image see from the front view. The arrow indicates the direction of scan.

Fig.1 Normal Scan



Fig.2 Reverse Scan



Fig. 1 Normal scan (pin 4, LR/UD = High or NC)

Fig. 2 Reverse scan (pin 4, LR/UD = Low)

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5. INTERFACE PIN DESCRIPTION

5.1 LCM Connector PIN Assignment

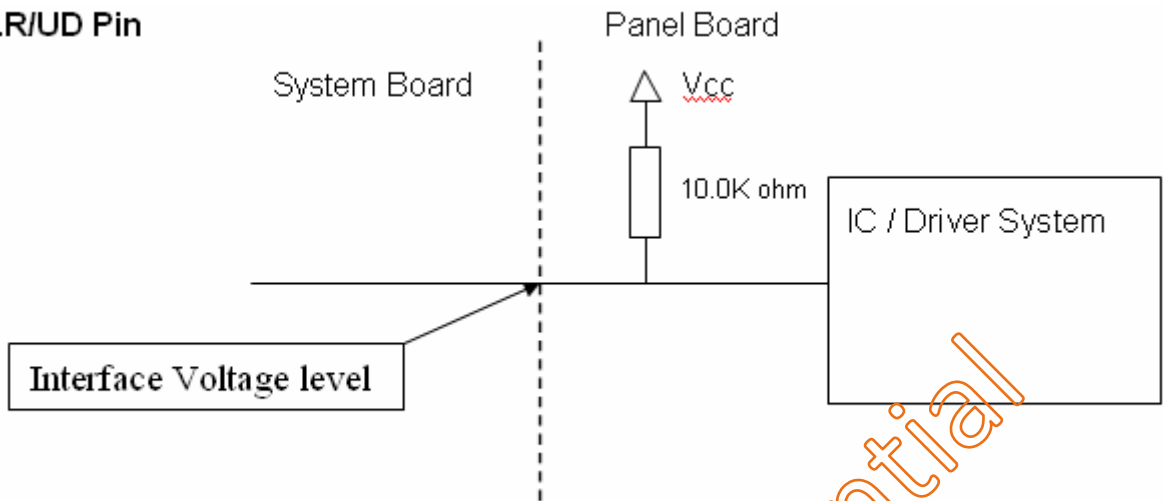
	Symbol	Description	Note
1	VCC	Power supply: +3.3V	-
2	VCC	Power supply: +3.3V	-
3	NC	No Connection	
4	LR/UD	Reverser Scan Control H or NC= Normal Mode L= Horizontal/Vertical Reverse Scan	-
5	RX0-	LVDS Differential Data Input (Negative)	
6	RX0+	LVDS Differential Data Input (Positive)	
7	GND	Ground	
8	RX1-	LVDS Differential Data Input (Negative)	
9	RX1+	LVDS Differential Data Input (Positive)	-
10	NC	No Connection	
11	RX2-	LVDS Differential Data Input (Negative)	
12	RX2+	LVDS Differential Data Input (Positive)	-
13	GND	Ground	-
14	RXCLK-	LVDS Differential Clock (Negative)	-
15	RXCLK+	LVDS Differential Clock (Positive)	-
16	GND	Ground	-
17	RX3-	LVDS Differential Data Input (Negative)	-
18	RX3+	LVDS Differential Data Input (Positive)	-
19	NC	No Connection	
20	SEL68	LVDS 6/8 bit select function control, High-> 6bit input mode Low or NC-> 8bit input mode	(3)

Note (1) Connector Part No.: Cvilux CID520D1HR0-NH or equivalent.

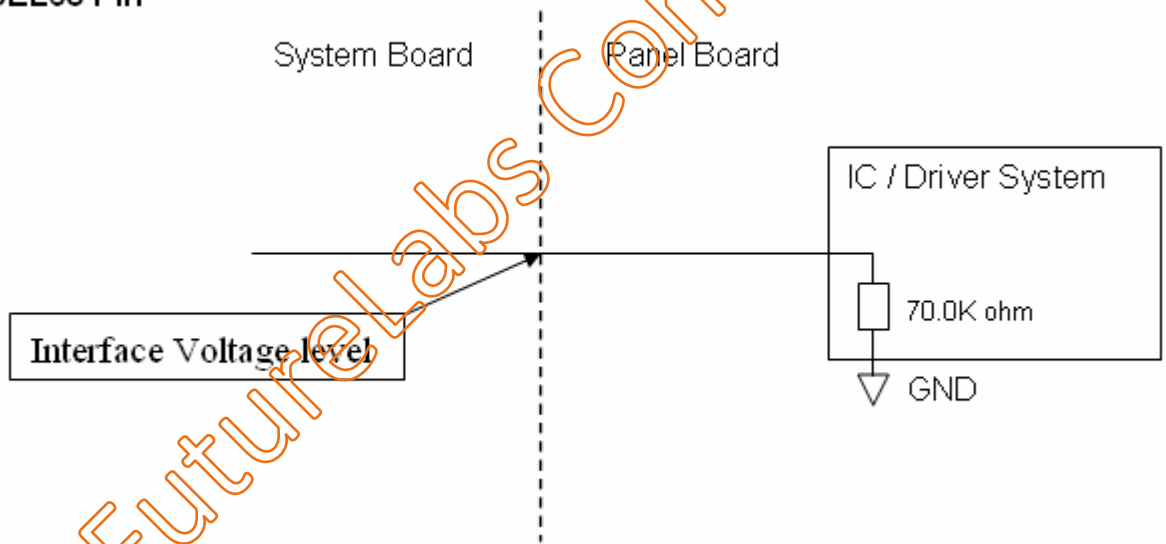
Note (2) User's connector Part No.: Hirose DF14-20S-1.25C or equivalent.

Note (3) "Low" stands for 0V. "High" stands for 3.3V. "NC" stands for "No Connection".

LR/UD Pin

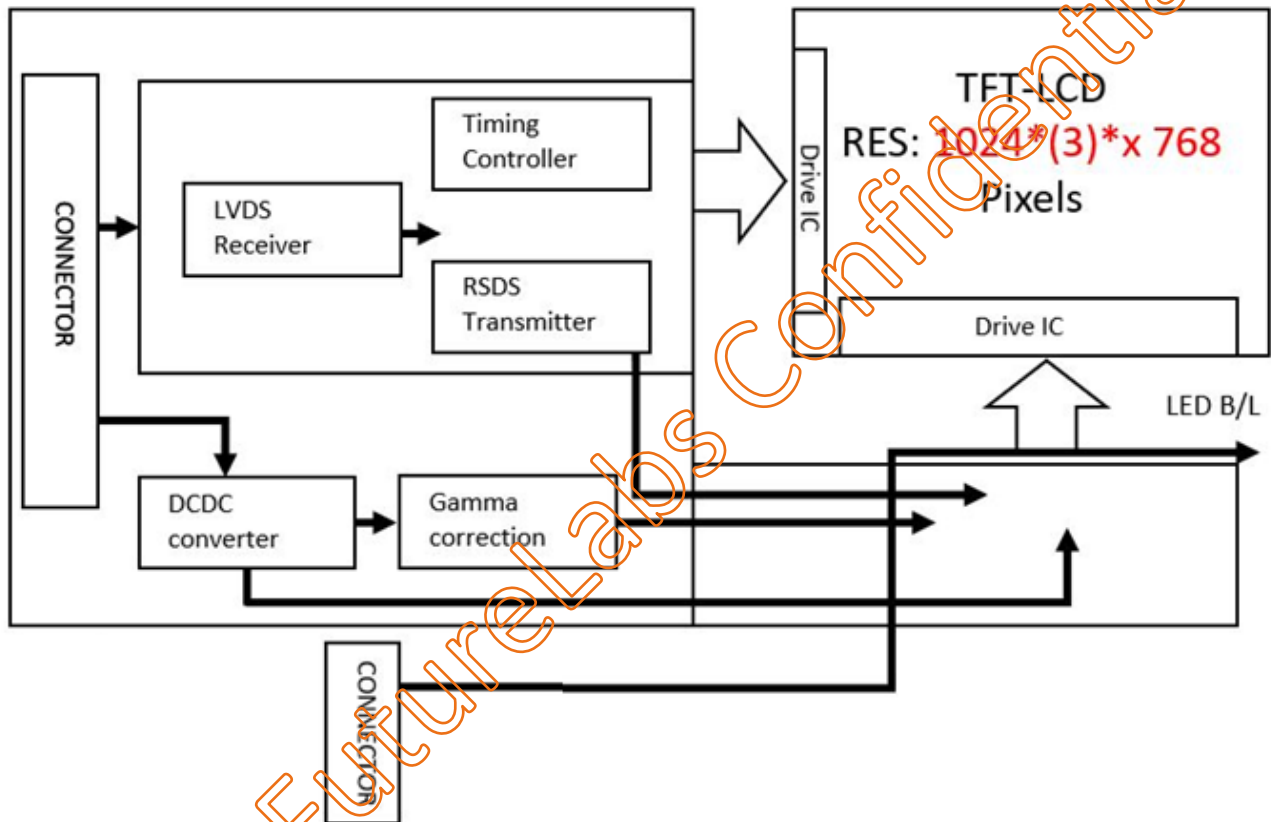


SEL68 Pin



6. BLOCK DIAGRAM

The following diagram shows the functional block of the TFT module:



7. OPTICAL CHARACTERISTIC

The optical characteristics are measured under stable conditions at room temperature.

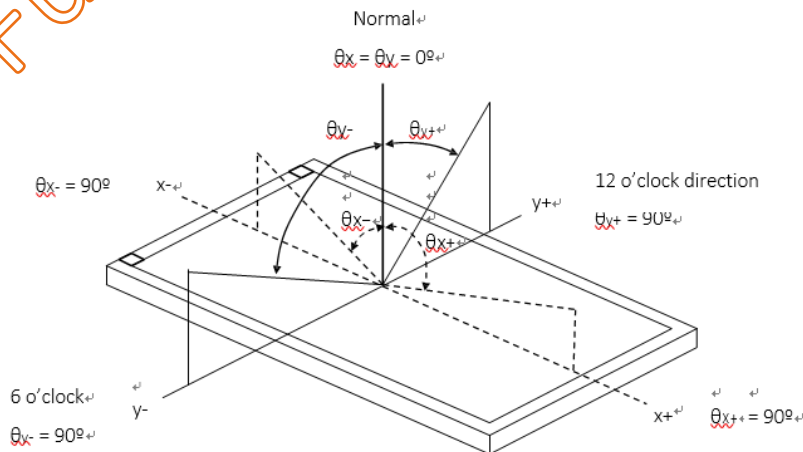
Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note	
Contrast Ratio		CR	$\theta_x=0^\circ, \theta_y=0^\circ$ Viewing angle at normal direction	1800	2500		-	(2)	
Response Time		TR		-	16	21	ms	(3)	
		TF		-	7	14			
Center Luminance of White		LC		1350	1500	-	cd/m ²	(4)	
White Variation		ΔW		-	1.25	1.33	-	(5)	
Chromaticity	Red	Rx		$\theta_x=0^\circ, \theta_y=0^\circ$ Viewing angle at normal direction	-0.05	0.647	+0.05	-	(6)
		Ry				0.338		-	
	Green	Gx				0.321		-	
		Gy				0.606		-	
	Blue	Bx				0.157		-	
		By	0.039			-			
	White	Wx	0.313			-			
		Wy	0.329			-			
Viewing Angle	Horizontal	θ_{x+}	CR \geq 10 USB2000	80	88	-	Deg.	(1)	
		θ_{x-}		80	88	-			
	Vertical	θ_{y+}		80	88	-			
		θ_{y-}		80	88	-			

The following optical specifications shall be measured in a darkroom or equivalent state (ambient luminance ≤ 1 lux, and at room temperature).

The room temperature is 25°C \pm 2°C.

Note 1: Definition of Viewing Angle

Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or the vertical clock direction with respect to the optical axis which is normal to the LCD surface

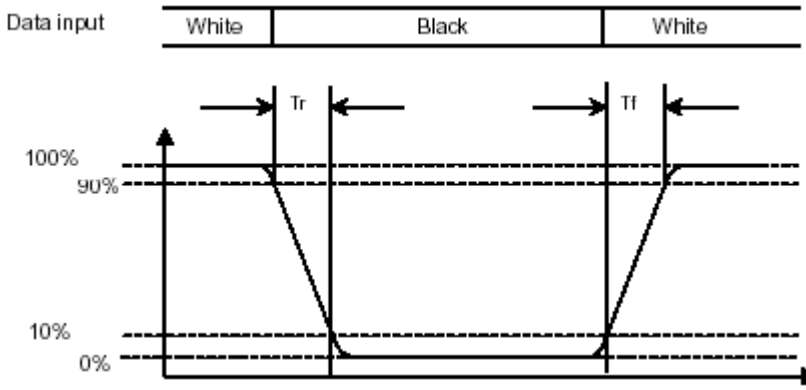


Note 2: Definition of Contrast Ratio (CR)

Measure the viewing angle of $\Theta = 0$ and at the center of the LCD surface. Luminance with all pixels in white state divide by Luminance with all pixels in Black state

Note 3: Definition of Response Time:

The response time is set initially by defining the “Rising Time (Tr)” and the “Falling Time (Tf)” respectively. The response time interval is between 10% and 90% of amplitudes, please refer the figure to the followings:



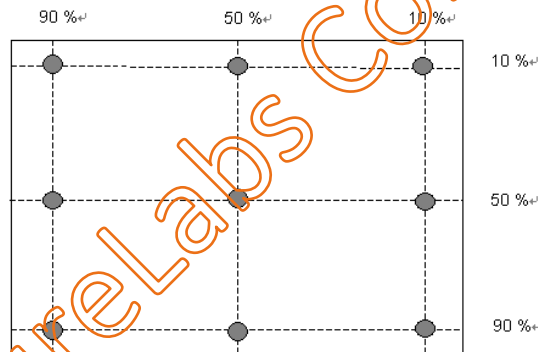
Note 4: Definition of Center Luminance of White

Measure the center area of the panel and the viewing angle of the $\theta_x = \theta_y = 0^\circ$

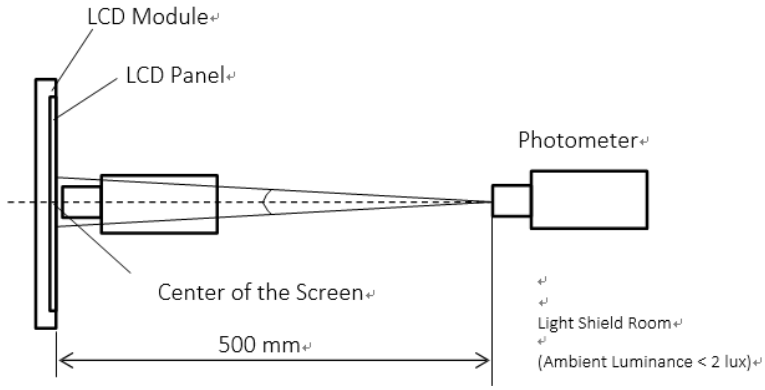
Note 5: Definition of White Variation (δW):

Measure the luminance of gray level 255 at 9 points

$$\delta W = \text{Maximum} [L(1), L(2), \dots, L(8), L(9)] / \text{Minimum} [L(1), L(2), \dots, L(8), L(9)]$$

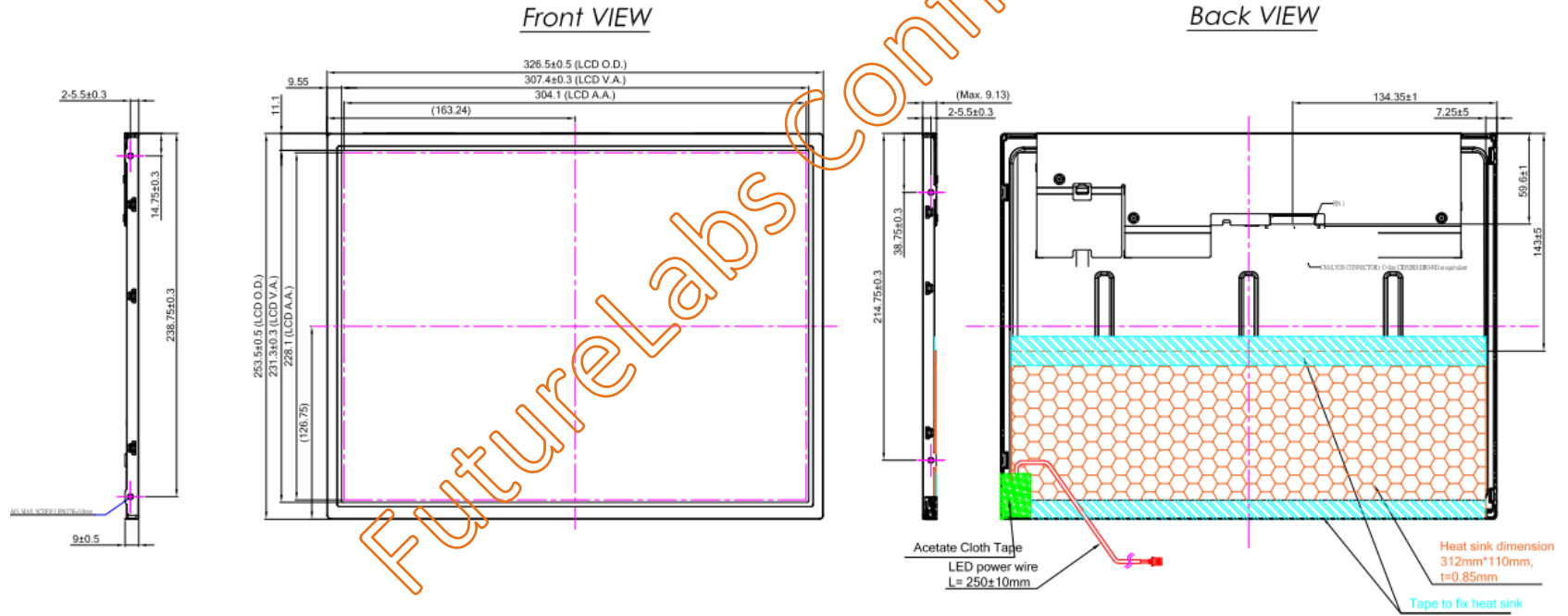


Note 6: The method of optical measurement:



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8. DIMENSION & DRAWING



NOTES:

- 1.General tolerance are ±0.5mm
- 2.ROHS complied

LCM CN1 Pin Define			
PIN	SYMBOL	PIN	SYMBOL
1	VCC	11	RX2-
2	VCC	12	RX2+
3	NC	13	GND
4	LR/UD	14	RXCLK-
5	RX0-	15	RXCLK+
6	RX0+	16	GND
7	GND	17	RX3-
8	RX1-	18	RX3+
9	RX1+	19	NC
10	NC	20	SEL68

Customer Approval	Part Number # REV	FLC-150GML6000SA1	#00	
Date	Rev. No	Rev. Date	Rev. Date	Rev. Date
Company	Company	Company	Company	Company
Name	Name	Name	Name	Name
Signature	Signature	Signature	Signature	Signature
File Drawing	Printed	Scale	Unit	Check

9. PRECAUTION AND PRODUCT HANDLING

- Do not apply the external force such as bending or twisting to the LCD panel and backlight during assembly.
- Do not insert and plug out the input connector while the LCD panel is operating.
- Do not take apart the panel or frame from LCD module assembly or insert anything into the backlight unit.
- Do not keep the same pattern in a long period of time, it may cause image sticking on LCD panel. Can use shuffle content periodically if fixed pattern is displayed on the screen.
- Do not touch the display area with bare hands, this will stain the display area.
- Pay attention to handle lead wire of backlight, that is not tugged in connect with LED driver.
- Do not change variable resistance settings in LCD panel, it may cause not satisfy of LCD characteristics specification.
- The surface of LCD panel's polarizer is very soft and easily scratched, please use a very soft dry cloth without chemicals for cleaning.
- To avoid the static electricity to damage the CMOS LSI, the operator should be grounded when in contact with the LCD panel, and also to all electrical equipment.
- Need to follow the correct power frequency when LCD panel is connecting and operating, this can avoid damage to CMOS LSI during latch-up.
- Need to store the LCD panel indoor without the exposure of sunlight where the temperature is $25^{\circ}\text{C} \pm 5^{\circ}\text{C}$ and the humidity is below 60% RH.