Product Specification

Part Number #REV: FLC-121GML2000SA1#00

Description: TFT 12.1'' 1024*768 Full View One Channel LVDS 600CD

( ) Preliminary Specification

(V) Approved Specification

Customer Name:	
Signature:	**Date:**

PREPARED BY	**REVIEWED BY**	**SIGNATURE DATE**
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Revision History

Version	Date	Page	Description	Note
V1.0	2020/08/06		First Edition	
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1. GENERAL DESCRIPTION

1.1 Description

12.1" is a Color Active Matrix Liquid Crystal Display Module composed of a TFT LCD panel and LED backlight system. The screen format is intended to support the XGA, 1024x768 screen and 262K / 16.7 M colors.

1.2 Product Summary

The following items are summary on the table under Ta=25 °C condition:

No.	Item	Specification	Unit
1	Display Size	12.1"	Inch
2	Pixel Number	1024 (H) x 3(RGB)x 768 (V)	Pixels
3	Outline Dimension	260.5(W)×204(H)×8.4(D, max)	mm
4	Active Area	245.76 (H) x 184.32 (V)	mm
5	Pixel Pitch	0.24 x0.24	mm
6	Display Colors	262K / 16.7M colors	
7	Pixel Arrangement	RGB vertical stripe	- -
8	Display Mode	Full View / Normally Black	- -
9	Electrical Interface	One Channel LVDS	- -
10	Surface Treatment	Anti-Glare, 3H Anti-Glare	- -
11	Brightness	600 (Typ.)	- -
12	Contrast Ratio	1000 (Typ.)	cd/m2
13	Total Power Consumption (Typ)	Total 12.02	W

2. ABSOLUTE MAXIMUM RATING

2.1 Electrical Absolute Rating

Item	Symbol	Values			Unit	Note
		Min	Typ	Max		
Logic/LCD Drive Voltage	Vcc	-0.3	--	4	V	(1)
Converter Voltage	V	-0.3	--	18	V	(1)(2)
Enable Voltage	EN	-	--	5.5	V	
Backlight Adjust	ASJ		--	5.5	V	

Note (1) Permanent damage to the device may occur if max values are exceeded.

Function operation should be restricted to the conditions described under normal operating conditions.

(2) Specified values are for input pin of LED light bar at Ta-25± 2ºC

2.2 Environment Absolute Rating

Item	Symbol	Values			Unit	Note
		Min.	Typ	Max.		
Operating Temperature	Top	-30		85	°C	Note(1)(2)
Storage Temperature	Tstg	-40		90	°C	

Note (1) : Temperature and relative humidity range is shown in the figure below.

(a) 90 %RH Max. (Ta ≦ 40 ºC).

(b) Wet-bulb temperature should be 39 ºC Max. (Ta > 40 ºC).

(c) No condensation.

Note(2) : The absolute maximum rating values of this product are not allowed to be exceeded at any times. The module should not be used over the absolute maximum rating value. It will cause permanently unrecoverable function fail in such an condition

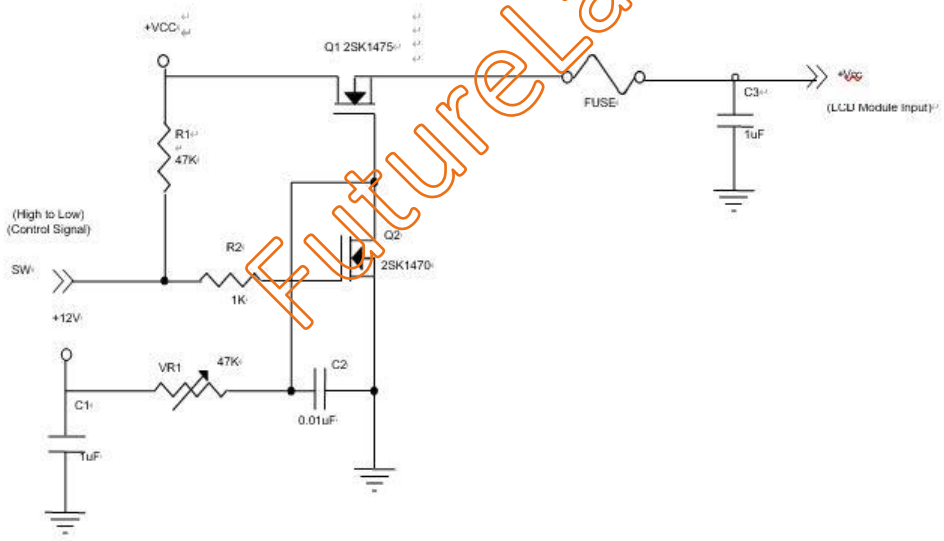
3. ELECTRICAL CHARACTERISTICS

3.1 LCD Electrical Specification

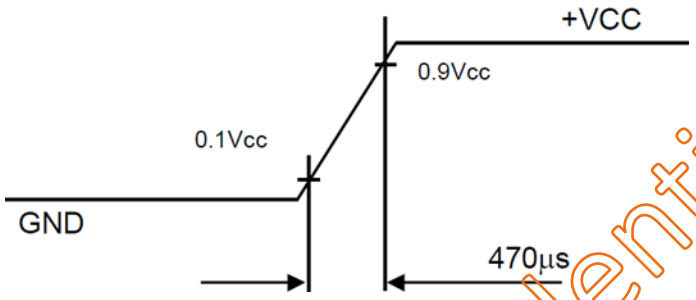
Parameter		Symbol	Value			Unit	Note
			Min.	Typ.	Max.		
Logic/LCD Input Voltage		VCC	3.0	3.3	3.6	V	VDD=3.3V
LCD Rush Current		IRUSH	--	--	4	A	(2)
Power Supply Current	White	VIH		370	450	mA	(3) VDD=3.3V
	Black	VIL		300	380	mA	(3) VDD=3.3V
LCD Power consumption		PL	--	1.22	1.49	W	
LVDS differential input voltage		VID	100	--	600	mV	
LVDS common input voltage		VICM	0.7	--	1.6	V	

Note (1) The assembly should be always operated within above ranges. Ta = 25 ± 2 ºC

Note (2) Measurement Conditions:

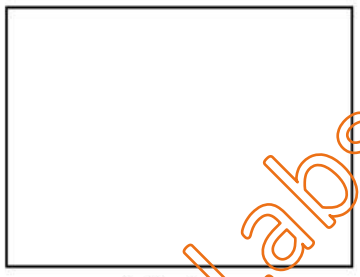


Vcc rising time is 470μs



Note (3) The specified power supply current is under the conditions at Vcc = 3.3V, Ta = 25 ± 2 ºC, f_v = 60 Hz, whereas a power dissipation check pattern below is displayed.

a. White Pattern



Active Area

b. Black Pattern

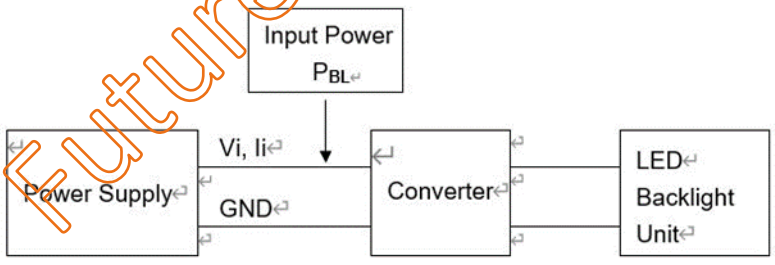
Active Area

3.3 Backlight Unit

Parameter guideline for LED driving is under stable conditions at 25℃ (Room Temperature):

Symbol	Parameter		Min.	Typ.	Max.	Unit	Remark
Vi	Converter Power Supply Voltage		10.8	12.0	13.2	V	
Virp	Converter Power Supply Ripple Voltage				500	mv	
Ii	Converter Power Supply Current			0.9	1.07	A	Vi=12V(100% Duty)
Iirush	Converter Jnrush Current				3.0	A	Vi rising time=10ms(Vi=12V)
PBL	Backlight Power Consumption			10.8	12.8	W	Vi =12V(Duty 100%)
BLON	EN Control Level	Backlight on	2.5	3.3	5.0	V	
		Backlight off	0	--	0.3	V	
E_PWM	PWM Control Level	High Level	2.5	3.3	5.0	V	
		Low Level	0	--	0.15	V	
VNoise	PWM Noise Range				0.1	V	
--	PWM Control Duty Ratio				100	%	@200Hz
fpwm	PWM Control Frequency		190	200	20k	Hz	(3)
LL	LED Lifetime		50,000	--	-	Hrs	(2)

Note (1) LED current is measured by utilizing a high frequency current meter as shown below:



Note (2) The lifetime of LED is estimated data and defined as the time when it continues to operate under the conditions at Ta = 25 ±2 ℃ and Duty 100% until the brightness becomes ≦ 50% of its original value.
Operating LED at high temperature condition will reduce lifetime and lead to color shift.
Note (3) At 200Hz PWM control frequency，duty ratio range is restricted from 1% to 100%, When PWM control frequency is 20kHz, duty ratio range is restricted from 10% to 100%.

4. COLOR DATA INPUT ASSIGNMENT

The brightness of each primary color (red, green and blue) is based on the 6-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of color versus data input

Color		Data Signal																	
		Red						Green						Blue					
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Colors	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
Gray	Red(2)	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Scale	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Of	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Red	Red(61)	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	Red(62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Green(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
Gray	Green(2)	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Scale	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Of	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Green	Green(61)	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
	Green(62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green(63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Blue(0)/Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
Gray	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Scale	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Of	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
Blue	Blue(61)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

Note (1) 0: Low Level Voltage, 1: High Level Voltage

The brightness of each primary color (red, green and blue) is based on the 8-bit gray scale data input for the color. The higher the binary input, the brighter the color. The table below provides the assignment of color versus data input.

Color		Data Signal																							
		Red								Green								Blue							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale Of Red	Red(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale Of Green	Green(0)/ Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	Green(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Gray Scale Of Blue	Blue(0) / Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

Note (1) 0: Low Level Voltage, 1: High Level Voltage

5. INTERFACE TIMING

5.1 INPUT SIGNAL TIMING SPECIFICATIONS

The input signal timing specifications are shown as the following table and timing diagram.

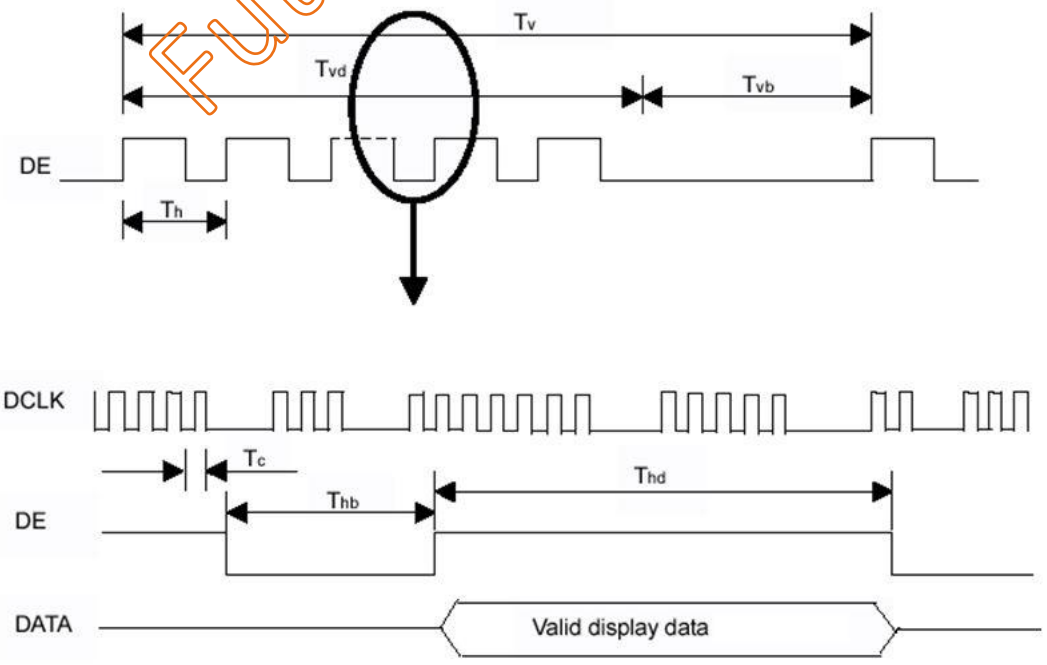
Signal	Item	Symbol	Min.	Typ.	Max.	Unit	Note
DCLK	Frequency	Fc	57.5	64.9	74.4	MHZ	
Vertical Active Display Term	Total	Tv	774	806	848	Th	Tv=Tvd+Tvb
	Display	Tvd	-	768	-	Th	-
	Blank	Tvb	6	38	80	Th	-
Horizontal Active Display Term	Total	Th	1240	1344	1464	Tc	Th=Thd+Thb
	Display	Thd	-	1024	-	Tc	-
	Blank	Thb	216	320	440	Tc	-

Note (1) Since this assembly is operated in DE only mode, Hsync and Vsync input signals should be set to low logic level. Otherwise, this assembly would operate abnormally.

Frame rate is 60Hz

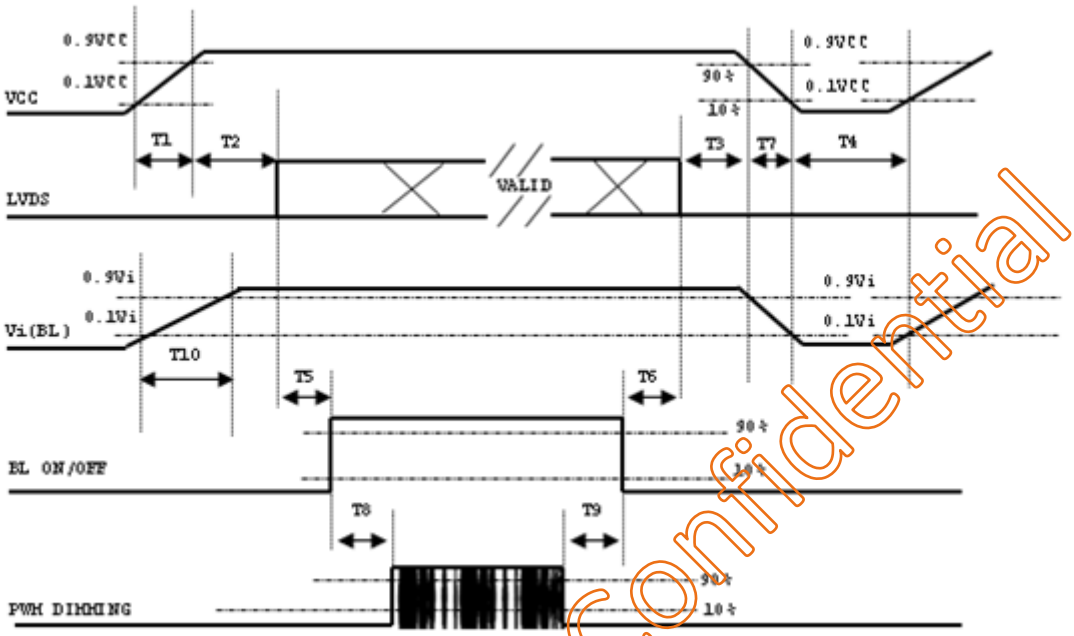
The Tv(Tvd+Tvb) must be integer, otherwise, this module would operate abnormally.

INPUT SIGNAL TIMING DIAGRAM



5.2 Power ON/OFF Sequence

VDD power and lamp on/off sequence is as below. Interface signals are also shown in the chart. Signals from any system shall be Hi-Z state or low level when VDD is off.



Note (1) Please avoid floating state of interface signal at invalid period.

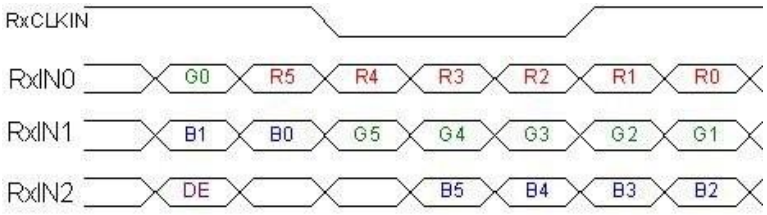
Note (2) When the interface signal is invalid, be sure to pull down the power supply of LCD VCC to 0 V.

Note (3) The Backlight converter power must be turned on after the power supply for the logic and the interface signal is valid. The Backlight converter power must be turned off before the power supply for the logic and the interface signal is invalid.

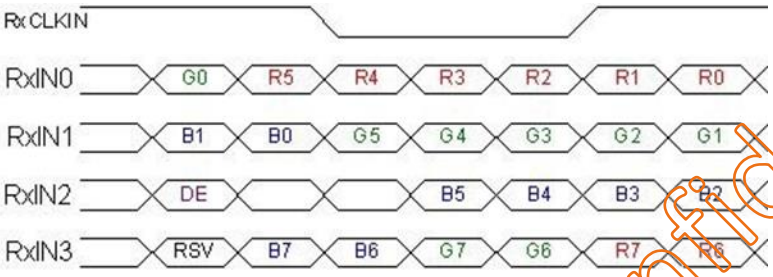
Parameter	Value			Units
	Min	Typ	Max	
T1	0.5	---	10	ms
T2	0	---	50	ms
T3	0	---	50	ms
T4	500	---	---	ms
T5	450	---	---	ms
T6	200	---	---	ms
T7	10	---	100	ms
T8	10	---	---	ms
T9	10	---	---	ms
T10	20	---	50	ms

5.3 The Input Data Format

SEL68 ="Low" or "NC" for 6 bits LVDS Input



SEL68 = "High" for 8 bits LVDS Input



Note (1) R/G/B data 7: MSB, R/G/B data 0: LSB

Note (2) Please follow PSWG

Signal Name	Description	Remark
R7	Red Data 7 (MSB)	Red-pixel Data
R6	Red Data 6	Each red pixel's brightness data consists of these
R5	Red Data 5	8 bits pixel data.
R4	Red Data 4	
R3	Red Data 3	
R2	Red Data 2	
R1	Red Data 1	
R0	Red Data 0 (LSB)	
G7	Green Data 7 (MSB)	Green-pixel Data
G6	GreenData 6	Each green pixel's brightness data consists of these
G5	GreenData 5	8 bits pixel data.
G4	GreenData 4	
G3	GreenData 3	
G2	GreenData 2	
G1	GreenData 1	
G0	GreenData 0 (LSB)	
B7	Blue Data 7 (MSB)	Blue-pixel Data
B6	Blue Data 6	Each blue pixel's brightness data consists of these
B5	Blue Data 5	8 bits pixel data.
B4	Blue Data 4	
B3	Blue Data 3	
B2	Blue Data 2	
B1	Blue Data 1	
B0	Blue Data 0 (LSB)	
RXCLKIN+ RXCLKIN-	LVDS Clock Input	
DE	Display Enable	
VS	Vertical Sync	
HS	Horizontal Sync	

Note (3) Output signals from any system shall be low or Hi-Z state when VDD is off.

5.4 Scanning Direction

The following figures show the image see from the front view.

The arrow indicates the direction of scan

Fig.1 Normal Scan

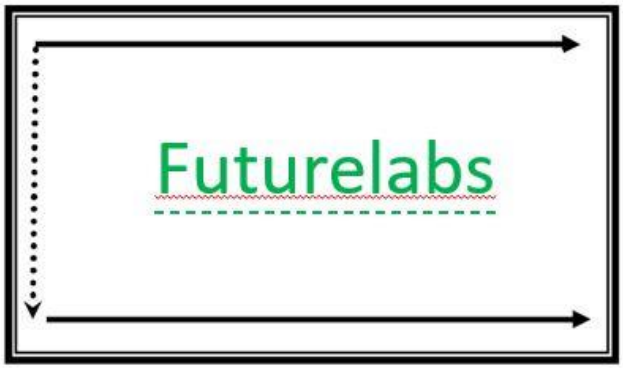


Fig.2 Reverse Scan

Fig.3 Reverse Scan

Fig.4 Reverse Scan

Fig. 1 Normal scan (pin 17, reLR=Low , pin 18, reUD= Low)

Fig. 2 Reverse scan (pin 17, reLR=High, pin 18, reUD= Low)

Fig. 3 Reverse scan (pin 17, reLR=Low , pin 18, reUD= High)

Fig. 4 Reverse scan (pin 17, reLR=High, pin 18, reUD= High)

6. INTERFACE PIN DESCRIPTION

6.1 LCM Connector PIN Assignment

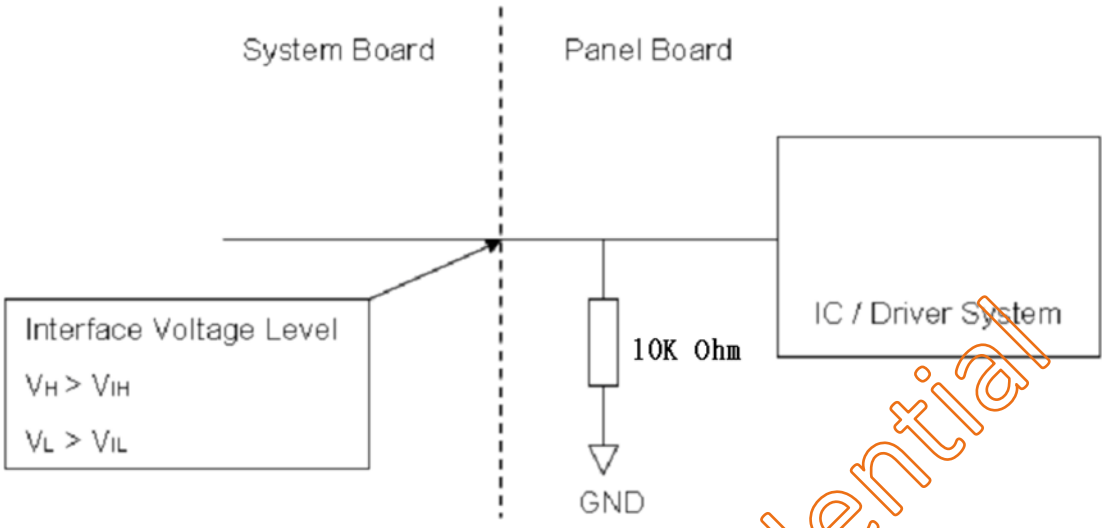
Pin	Name	Description	Remark
1	RX3+	Differential Data Input, CH3 (Positive)	
2	RX3-	Differential Data Input, CH3 (Negative)	
3	NC	NC	
4	SEL68	LVDS 6/8 bit select function control, Low => 6 bit Input Mode High => 8bit Input Mode	Note (3) (4)
5	GND	Ground	
6	RXC+	Differential Clock Input (Positive)	
7	RXC-	Differential Clock Input (Negative)	
8	GND	Ground	
9	RX2+	Differential Data Input , CH2 (Positive)	
10	RX2-	Differential Data Input , CH2 (Negative)	
11	GND	Ground	
12	RX1+	Differential Data Input , CH1 (Positive)	
13	RX1-	Differential Data Input, CH1 (Negative)	
14	GND	Ground	
15	RX0+	Differential Data Input, CH0 (Positive)	
16	RX0-	Differential Data Input, CH0 (Negative)	
17	reLR	Horizontal Reverse Scan Control, Low => Normal Mode. High => Horizontal Reverse Scan	Note (3) (4)
18	reUD	Vertical Reverse Scan Control, Low => Normal Mode, High =>Vertical Reverse Scan	Note (3) (4)
19	VCC	Power supply	
20	VCC	Power supply	

Note (1) Connector Part No.: STARCONN 076B20-0048RA-G4 or equivalent.

Note (2) User's connector Part No.: JAE FI-SE20ME or equivalent.

Note (3) "Low" stands for 0V. "High" stands for 3.3V.

Note (4) SEL68, reLR, reUD



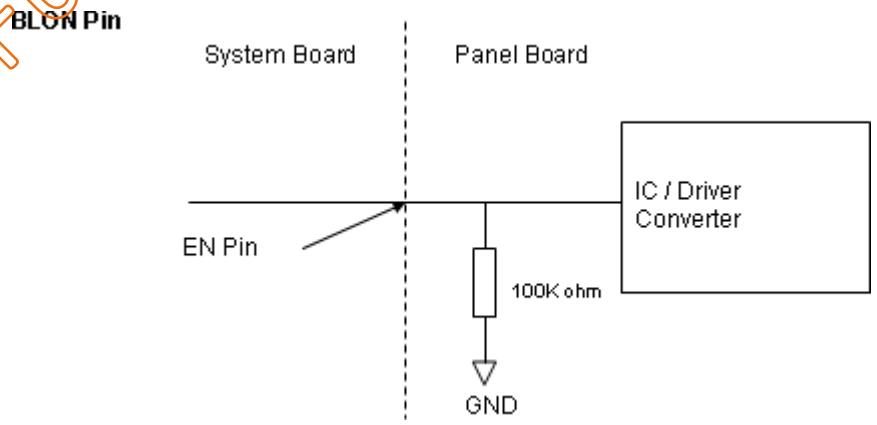
6.2 BACKLIGHT UNIT(Converter connector pin)

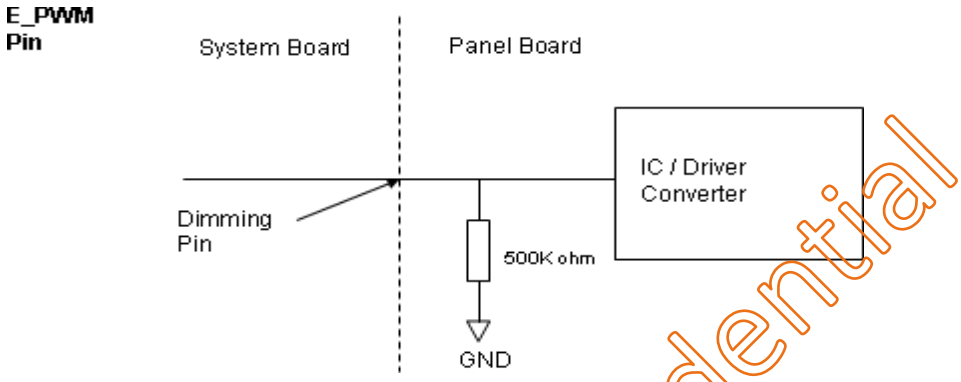
Pin	Symbol	Description	Remark
1	V_i	Converter input voltage	12V
2	V_i	Converter input voltage	12V
3	V_i	Converter input voltage	12V
4	V_i	Converter input voltage	12V
5	V_{GND}	Converter ground	Ground
6	V_{GND}	Converter ground	Ground
7	V_{GND}	Converter ground	Ground
8	V_{GND}	Converter ground	Ground
9	EN	Enable pin	3.3V, Note (3)
10	ADJ	Backlight Adjust	PWM Dimming (190-210Hz, Hi: 3.3V_{DC}, Lo: 0V_{DC}), Note (3)

Note (1) Connector Part No.: 91208-01001-H01 (ACES) or equivalent.

Note (2) User's connector Part No.: 91209-01011 (ACES) or equivalent

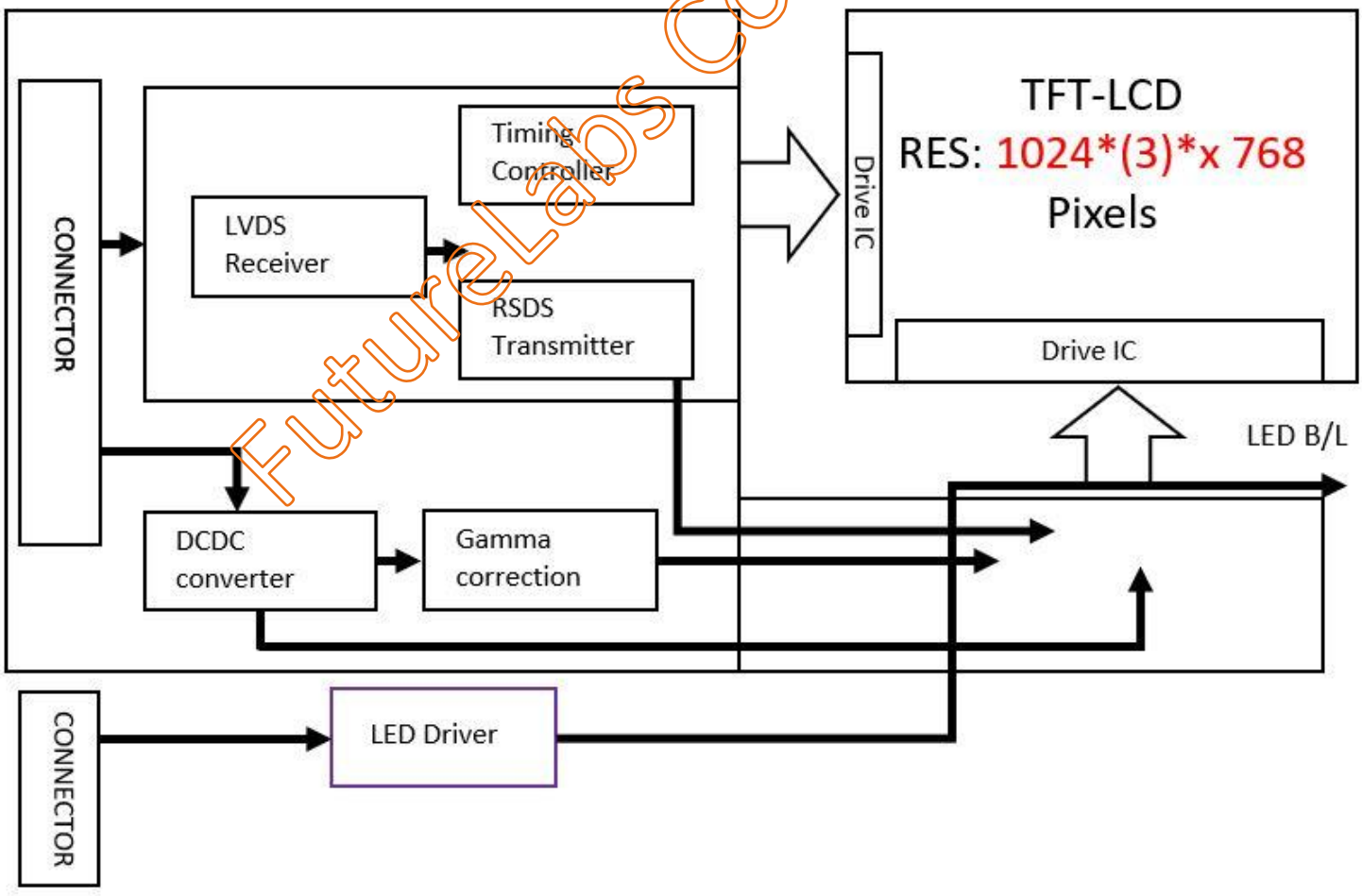
Note (3) EN(BLON), ADJ(E_PWM) as shown below :





7. BLOCK DIAGRAM

The following diagram shows the functional block of the TFT module:



8. OPTICAL CHARACTERISTIC

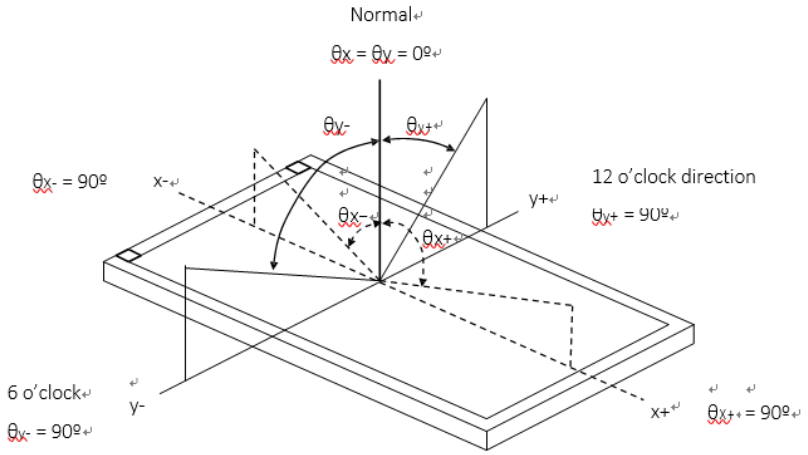
The optical characteristics are measured under stable conditions at room temperature.

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast Ratio		CR	θ_X=0°, θ_Y =0° Viewing angle at normal direction	700	1000		-	(2)(5)
Response Time		T_R		-	13	18	ms	(3)
		T_F		-	12	17	ms	
Center Luminance of White		L_C		480	600	-	cd/m2	(4)(5)
White Variation		δw		--	1.25	1.4	%	(5)(6)
Chromaticity	Red	Rx		Typ. -0.05	0.652	Typ. +0.05	-	(1) (5)
		Ry			0.338		-	
	Green	Gx			0.324		-	
		Gy			0.607		-	
	Blue	Bx			0.153		-	
		By			0.048		-	
	White	Wx			0.313		-	
		Wy			0.329		-	
Viewing Angle	Horizontal	θ_X+	CR=10	85	89	-	Deg.	(1)(5)
		θ_X-		85	89	-		
	Vertical	θY+		85	89	-		
		θY-		85	89	-		

The following optical specifications shall be measured in a darkroom or equivalent state (ambient luminance <2 lux, and at room temperature). The room temperature is 25°C±2°C.

Note 1: Definition of Viewing Angle

Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or the vertical clock direction with respect to the optical axis which is normal to the LCD surface

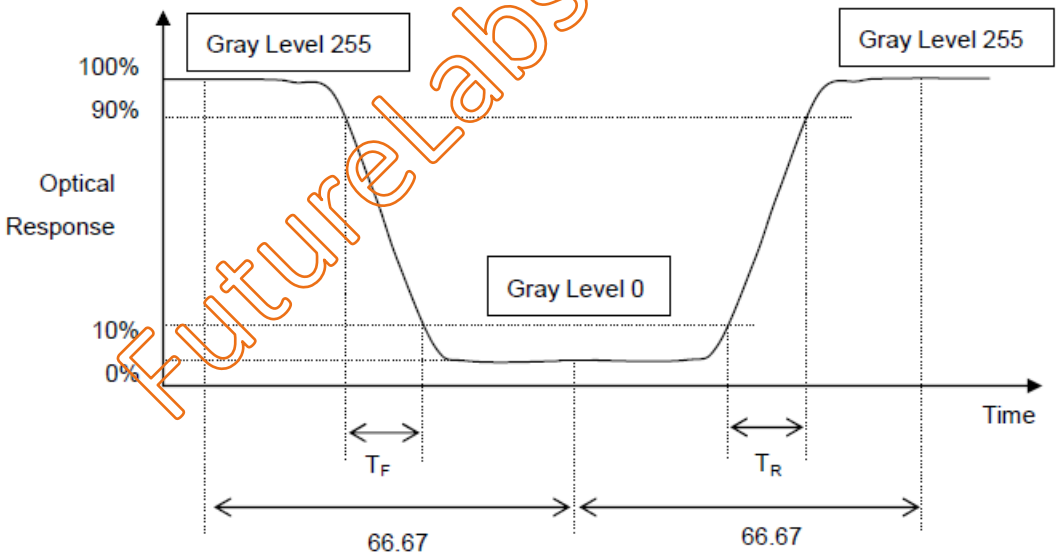


Note 2: Definition of Contrast Ratio (CR)

Measure the viewing angle of Θ= 0 and at the center of the LCD surface. Luminance with all pixels in white state divide by Luminance with all pixels in Black state

Note 3: Definition of Response Time:

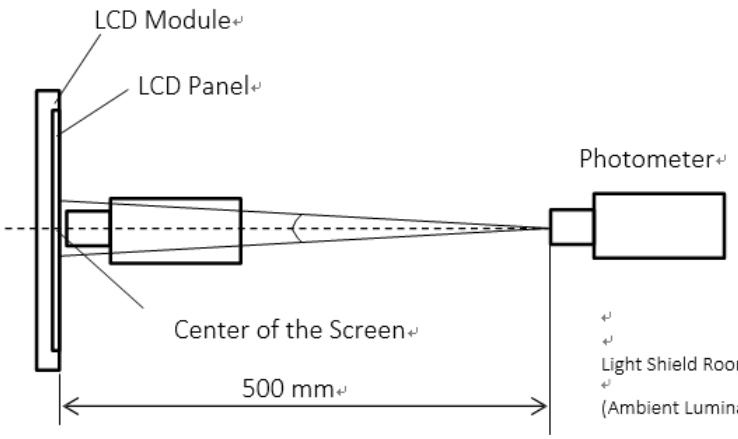
The response time is set initially by defining the "Rising Time (T_R)" and the "Falling Time (T_F)" respectively. Please refer the figure to the followings:



Note 4: Definition of Brightness (L)

Measure the center area of the panel and the viewing angle of the θx=θy =0°

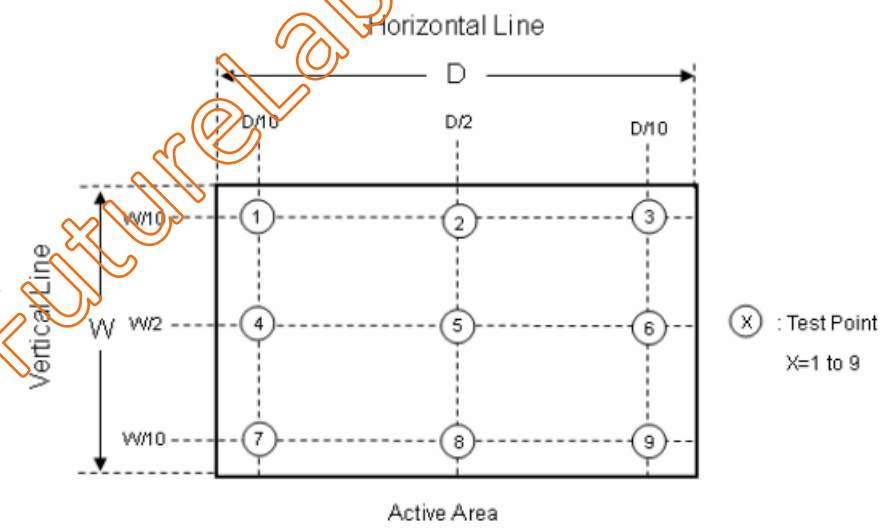
Note 5: The method of optical measurement:



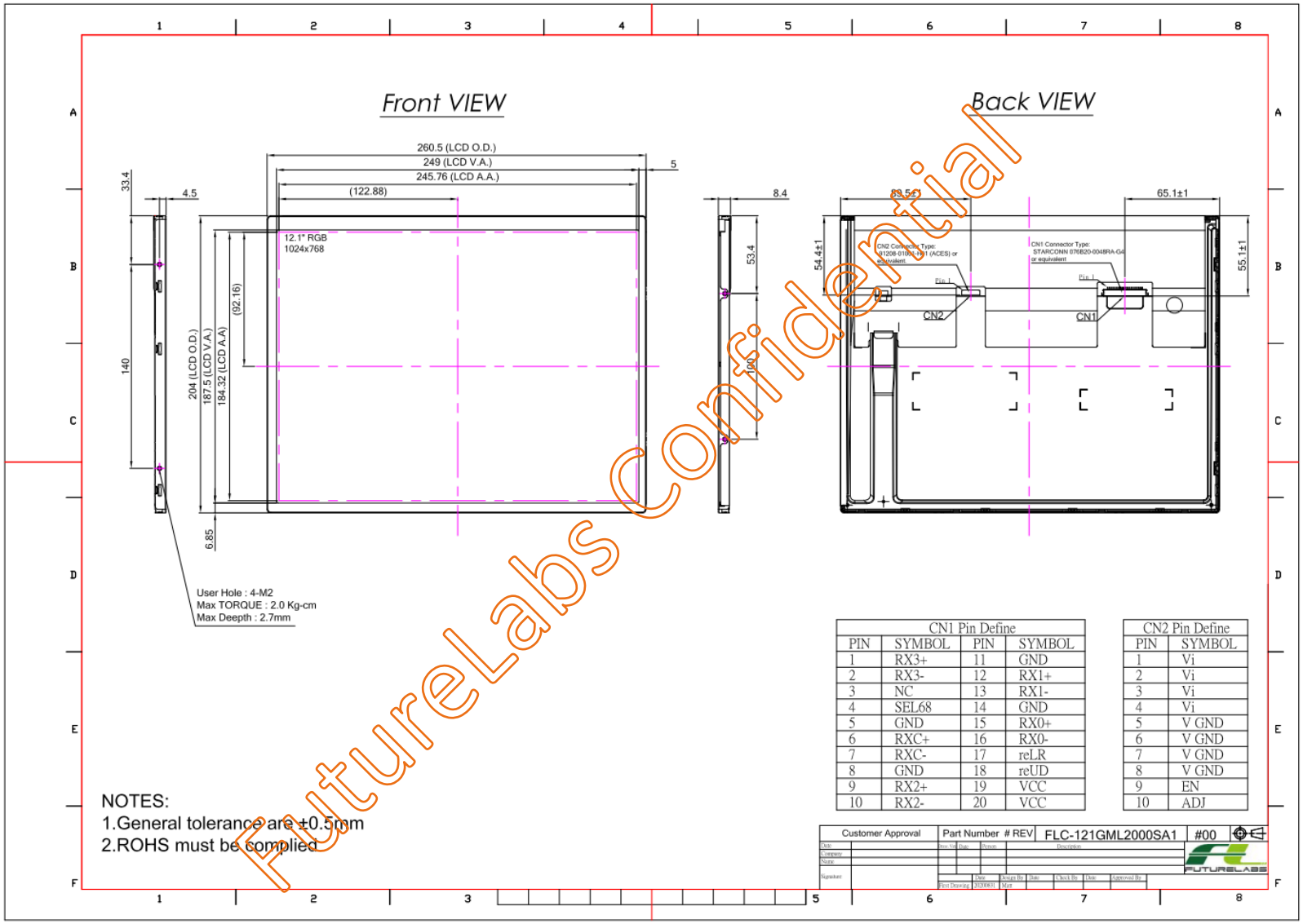
Note 6: Definition of White Variation (δW):

Measure the luminance of gray level 255 at 5 points

δW = Maximum [L (1), L (2), L (3), L (4)~ L (9)] / Minimum [L (1), L (2), L (3), L (4)~ L (9)]



9. DIMENSION AND DRAWING



CN1 Pin Define

PIN	SYMBOL	PIN	SYMBOL
1	RX3+	11	GND
2	RX3-	12	RX1+
3	NC	13	RX1-
4	SEL68	14	GND
5	GND	15	RX0+
6	RXC+	16	RX0-
7	RXC-	17	reLR
8	GND	18	reUD
9	RX2+	19	VCC
10	RX2-	20	VCC

CN2 Pin Define

PIN	SYMBOL
1	Vi
2	Vi
3	Vi
4	Vi
5	V GND
6	V GND
7	V GND
8	V GND
9	EN
10	ADJ

10. PRECAUTION AND PRODUCT HANDLING

- Do not apply the external force such as bending or twisting to the LCD panel and backlight during assembly.
- Do not insert and plug out the input connector while the LCD panel is operating.
- Do not take apart the panel or frame from LCD module assembly or insert anything into the backlight unit.
- Do not keep the same pattern in a long period of time, it may cause image sticking on LCD panel. Can use shuffle content periodically if fixed pattern is displayed on the screen.
- Do not touch the display area with bare hands, this will stain the display area.
- Pay attention to handle lead wire of backlight, that is not tugged in connect with LED driver.
- Do not change variable resistance settings in LCD panel, it may cause not satisfy of LCD characteristics specification.
- The surface of LCD panel's polarizer is very soft and easily scratched, please use a very soft dry cloth without chemicals for cleaning.
- To avoid the static electricity to damage the CMOS LSI, the operator should be grounded when in contact with the LCD panel, and also to all electrical equipment.
- Need to follow the correct power frequency when LCD panel is connecting and operating, this can avoid damage to CMOS LSI during latch-up.
- Need to store the LCD panel indoor without the exposure of sunlight where the temperature is 25°C ± 5°C and the humidity is below 60% RH.