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SHENZHEN FRIDA LCD CO.,LTD

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REV : A

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SPEC TITLE  
DOCUMENT CONTROL SPECIFICATION

EFFECTIVE DATE : 2018-09-30

# PRODUCT SPECIFICATION

## TFT-LCD MODULE

**Model No: FRD240T18005-B**

For Customer's Acceptance	
Approved by	Comment

	Signature	Date
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### 1. Document Revision History :

DOCUMENT REVISION	DATE	DESCRIPTION	PREPARED BY
A	2018-09-30	First Release.	



## 2. General Description

No	Item	Specification	Remark
1	Screen Size	2.4 inch	
2	Display Mode	Normally White	
3	Resolution	240 × RGB × 320	
4	Active Area	36.72*48.96	mm
5	Outline Dimension	42.72*60.26*2.35	mm
6	Viewing Direction	6 o' clock	
7	Driver IC	ILI9341V	
8	Interface	SPI	
9	Back Light	White Led*4	
11	Touch Panel	-	





## 4. Interface Specification

Pin No	Symbol	Description	Note
1	GND	Ground.	
2	RESET	Reset Signal input pin.	
3	SCL	Serial clock signal.	
4	D/C	Register selection signal.	
5	CS	Chip selection signal.	
6	SDA	Serial data input pin.	
7	SDO	Serial data output pin.	
8	GND	Ground.	
9	VCC	Power Supply For LCD.	
10	LEDA	Power Supply For LED Backlight Anode Input.	
11~14	LED1- ~LED4-	Power Supply For LED Backlight Cathode Input.	
15	XL	No Connection.	
16	YU	No Connection.	
17	XR	No Connection.	
18	YD	No Connection.	



## 5. Absolute Maximum Ratings

### Electrical Maximum Ratings – for IC Only

Parameter	Symbol	Min.	Max.	Unit	Note
Power supply voltage (VCC)	VCC	-0.3	+4.6	V	1

Note:

- 1.VCC, GND must be maintained.
- 2.The modules may be destroyed if they are used beyond the absolute maximum ratings.

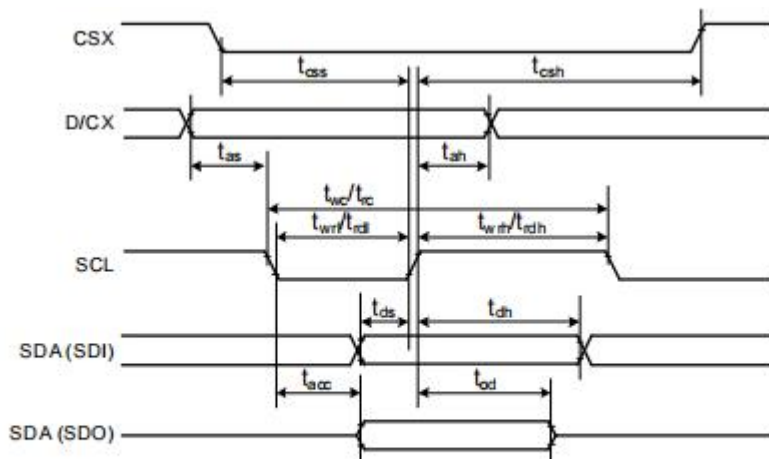
## 6. Electrical Specifications

At Ta = 25 °C, VCC = 2.5V to 3.3V, GND=0V.

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Supply voltage (analog)	VCC-GND		2.5	2.8	3.3	V
Supply current (Logic & LCD)	ICC		-	-	-	mA
Supply voltage of white LED backlight	VLED	Forward current =80mA Number of LED =4	2.7	3.0	3.3	V

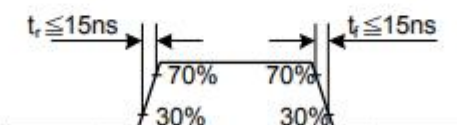
## 7. Timing Characteristics

### 7.1 Display Serial Interface Timing Characteristics (4-line SPI system)



Signal	Symbol	Parameter	min	max	Unit	Description
CSX	$t_{css}$	Chip select time (Write)	40	-	ns	
	$t_{csh}$	Chip select hold time (Read)	40	-	ns	
SCL	$t_{wc}$	Serial clock cycle (Write)	100	-	ns	
	$t_{wrh}$	SCL "H" pulse width (Write)	40	-	ns	
	$t_{wrl}$	SCL "L" pulse width (Write)	40	-	ns	
	$t_{rc}$	Serial clock cycle (Read)	150	-	ns	
	$t_{rdh}$	SCL "H" pulse width (Read)	60	-	ns	
	$t_{rdl}$	SCL "L" pulse width (Read)	60	-	ns	
D/CX	$t_{as}$	D/CX setup time	10	-		
	$t_{ah}$	D/CX hold time (Write / Read)	10	-		
SDA / SDI (Input)	$t_{ds}$	Data setup time (Write)	30	-	ns	
	$t_{dh}$	Data hold time (Write)	30	-	ns	
SDA / SDO (Output)	$t_{acc}$	Access time (Read)	10	-	ns	For maximum CL=30pF
	$t_{od}$	Output disable time (Read)	10	50	ns	For minimum CL=8pF

Note:  $T_a = 25^\circ\text{C}$ ,  $V_{DD}=1.65\text{V to }3.3\text{V}$ ,  $V_{CI}=2.5\text{V to }3.3\text{V}$ ,  $AGND=V_{SS}=0\text{V}$





## 8.Power Supply Configuration

VDDI and VCI can be applied in any order.

VCI and VDDI can be powered down in any order.

During power off, if LCD is in the Sleep Out mode, VCI and VDDI must be powered down minimum 120msec after RESX has been released.

During power off, if LCD is in the Sleep In mode, VDDI or VCI can be powered down minimum 0msec after RESX has been released.

CSX can be applied at any timing or can be permanently grounded. RESX has priority over CSX.

*Note 1: There will be no damage to the display module if the power sequences are not met.*

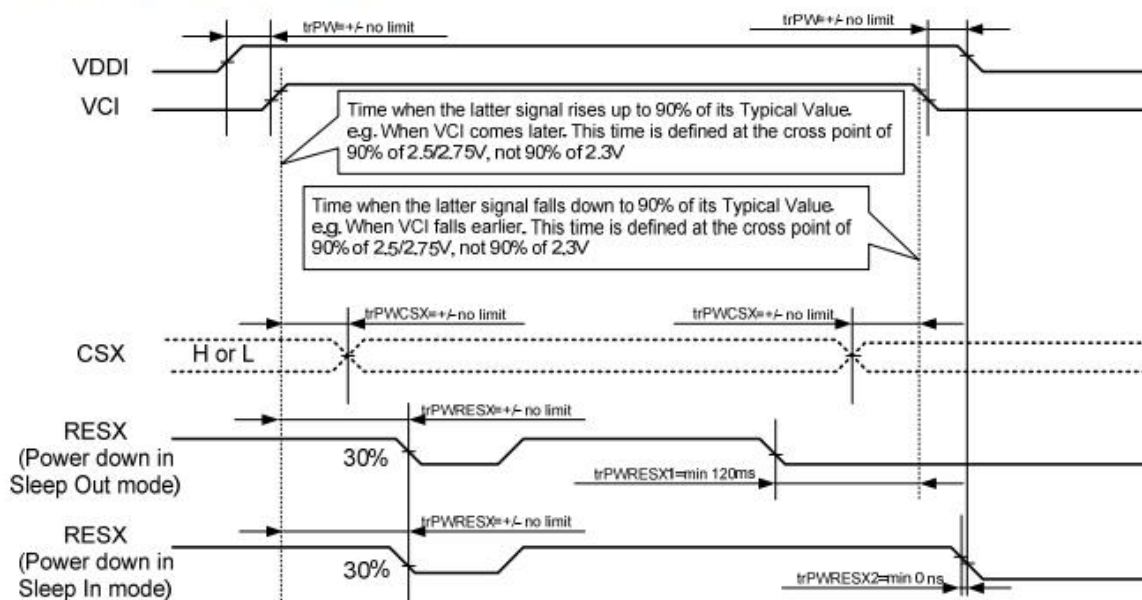
*Note 2: There will be no abnormal visible effects on the display panel during the Power On/Off Sequences.*

*Note 3: There will be no abnormal visible effects on the display between end of Power On Sequence and before receiving Sleep Out command. Also between receiving Sleep In command and Power Off Sequence.*

*Note 4: If RESX line is not held stable by host during Power On Sequence as defined in Sections 12.1 and 12.2, then it will be necessary to apply a Hardware Reset (RESX) after Host Power On Sequence is complete to ensure correct operation. Otherwise function is not guaranteed.*

### 8.1. Case 1 – RESX line is held High or Unstable by Host at Power ON

If RESX line is held High or unstable by the host during Power On, then a Hardware Reset must be applied after both VCI and VDDI have been applied – otherwise correct functionality is not guaranteed. There is no timing restriction upon this hardware reset.

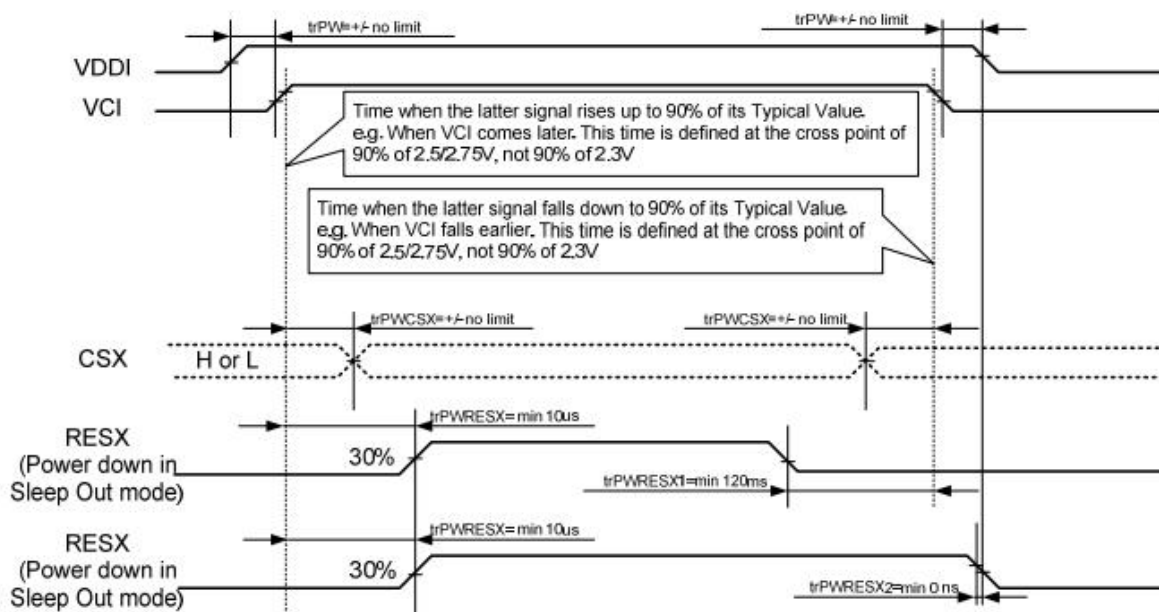


trPWRESX1 is applied to RESX falling in the Sleep Out Mode  
trPWRESX2 is applied to RESX falling in the Sleep In Mode

*Note 1: Unless otherwise specified, timings herein show cross point at 50% of signal power level.*

## 8.2. Case 2 – RESX line is held Low by Host at Power ON

If RESX line is held Low (and stable) by the host during Power On, then the RESX must be held low for minimum 10µsec after both VCI and VDDI have been applied.



trPWRESX1 is applied to RESX falling in the Sleep Out Mode  
trPWRESX2 is applied to RESX falling in the Sleep In Mode

Note 1: Unless otherwise specified, timings herein show cross point at 50% of signal power level.

## 8.3. Uncontrolled Power Off

The uncontrolled power off means a situation when e.g. there is removed a battery without the controlled power off sequence. There will not be any damages for the display module or the display module will not cause any damages for the host or lines of the interface. At an uncontrolled power off event, ILI9341V will force the display to blank and will not be any abnormal visible effects with in 1 second on the display and remains blank until "Power On Sequence" actives.



## 9.Optical Specification

Item 项目	Symbol 符号	Condition 条件	Min 最小值	Typ 典型值	Max 最大值	Unit 单位	Note 备注
Response time 响应时间	Tr+Tf	$\Theta=0^{\circ}$ $\emptyset=0^{\circ}$ Ta=25°C	-	20	30	ms	1
Contrast ratio 对比度	Cr		400	500	-	-	2
Color gamut 饱和度	S(%)		50	60	-	%	-
Luminance uniformity 均匀度	$\delta$ WHITE		80	-	-	%	3
Viewing angle range 视角范围	$\Theta_{x+}$	CR $\geq$ 10 Ta=25°C	-	65	-	deg	4
	$\Theta_{x-}$		-	65	-	deg	
	$\Theta_{y+}$		-	70	-	deg	
	$\Theta_{y-}$		-	40	-	deg	
LCM Luminance LCM 亮度	Lv	$\Theta=0^{\circ}$ $\emptyset=0^{\circ}$ Ta=25°C	-	230	-	Cd/m <sup>2</sup>	5
CIE (X,Y) Chromaticity 色度坐标	White(X)		0.25	0.28	0.31	-	6
	White(Y)		0.26	0.29	0.32	-	

Note1.Response time is the time required for the display to transition from White to black(Rise Time,Tr)and from black to white(Decay Time,Tf).For additional information see FIG1...

Note2.contrast Ratio(CR) is defined mathematically by the following formula ,For more information see FIG2.

Contrast Ratio(CR)=Average Surface Luminance with all white pixels/ Average Surface Luminance with all black pixels

Note3.The uniformity in surface luminance(WHITE) is determined by measuring luminance at each test position,and then dividing the maximum luminance of all white pixels by minimum luminance of all white pixels,For more information seeFIG2.

WHITE=Minimum Surface Luminance with all white pixels(P1,P2,.....)/Maximum Surface Luminance with all white pixels(P1,P2,.....)

Note4. Viewing angle is the angel at which contrast ratio is greater than a specific value. For TET module, the specific value of contrast ratio is 10. For monochrome and color stn module, the specific value of contrast ratio is 2. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG3

Note5. Surface luminance is the LCD surface luminance with all white pixels, For more information see FIG2.

LV=Average Surface Luminance with all white pixels(P1,P2,.....)

Note6. CIE(X,Y)chromaticity is the Center point value. For more information see FIG2.

Note7. For Viewing angle and response time testing, the testing date is base on Autronic-Melchers' s ConScope. Series instruments. For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing date is base on CS-2000 photo detector.

Note8. For TN type TFT transmissive module, Gray scale reverse occurs in the direction of panel viewing angle

FIG1. The definition of Response time

响应时间定义



FIG2. Measuring method for Contrast ratio, surface luminance, Luminance

uniformity, CIE(X,Y)chromaticity.

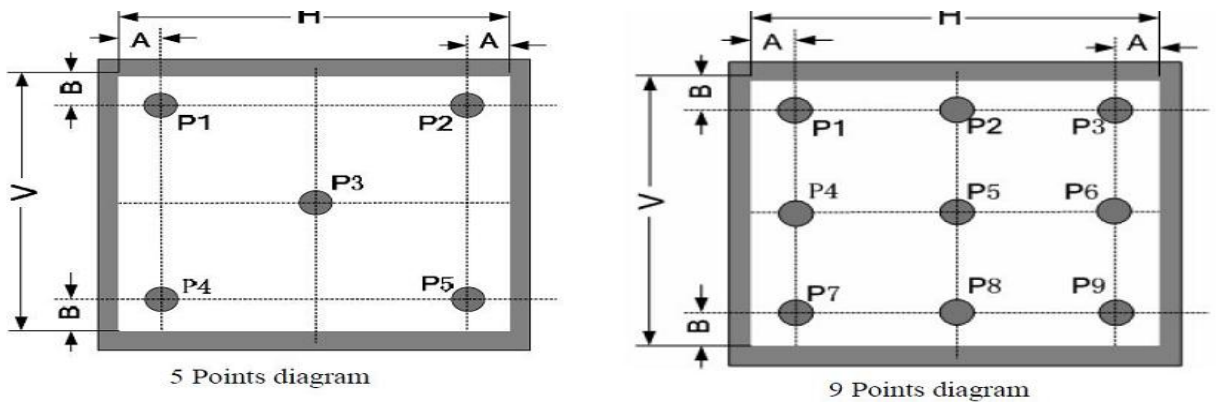
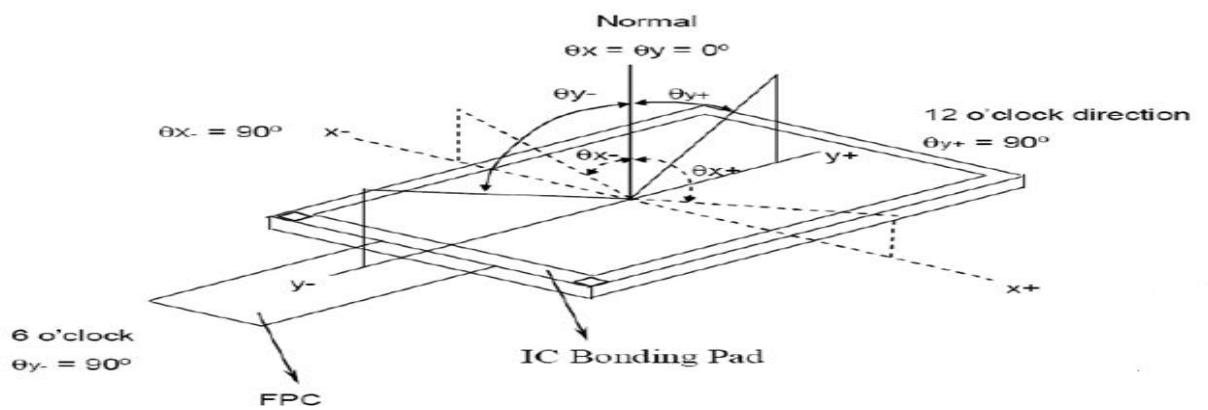


FIG3 The definition of viewing angle 视角定义







## 10. Reliability Test Items

Item	Test Condition	Criterion
High Temperature Storage	70 °C, 48 hrs	Note1,Note2
Low Temperature Storage	-20 °C, 48 hrs	
High Temp. & High Humidity Storage	40 °C, 80% RH, 48hrs	
Thermal Shock (Static)	-20°C, 30 min /70°C, 30 min, 20 cycles	
High Temperature Operation	60 °C, 48 hrs	
Low temperature Operation	-10 °C, 48 hrs	

Note1: Evaluation should be tested after storage at room temperature for two hours.

Note2:

Pass: Normal display image no line defect.

Fail: No display image, or line defects.

Partial transformation of the module parts should be ignored.

## 11. Precautions

Please pay attentions to the followings as using the LCD module.

### Handling

- (a) Do not apply strong mechanical stress like drop, shock or any force to LCD module. It may cause improper operation, even damage.
- (b) Because the polarizer is very fragile and easy to be damaged, do not hit, press or rub the display surface with hard materials.
- (c) Do not put heavy or hard material on the display surface, and do not stack LCD modules.
- (d) If the display surface is dirty, please wipe the surface softly with cotton swab or clean cloth.



- (e) Avoid using Ketone type materials (e.g. Acetone), Toluene, Ethyl acid or Methyl chloride to clean the display surface. It might damage the touch panel surface permanently. The recommended solvents are water and Isopropyl alcohol.
- (f) Wipe off water droplets or oil immediately.
- (g) Protect the LCD module from ESD. It will damage the LSI and the electronic circuit.
- (h) Do not touch the output pins directly with bare hands.
- (i) Do not disassemble the LCD module.
- (j) Do not lift the FPC of Touch Panel.

### **Storage**

- (a) Do not leave the LCD modules in high temperature, especially in high humidity for a long time.
- (b) Do not expose the LCD modules to sunlight directly.
- (c) The liquid crystal is deteriorated by ultraviolet. Do not leave it in strong ultraviolet ray for a long time.
- (d) Avoid condensation of water. It may cause improper operation.
- (e) Please stack only up to the number stated on carton box for storage and transportation. Excessive weight will cause deformation and damage of carton box.

### **Operation**

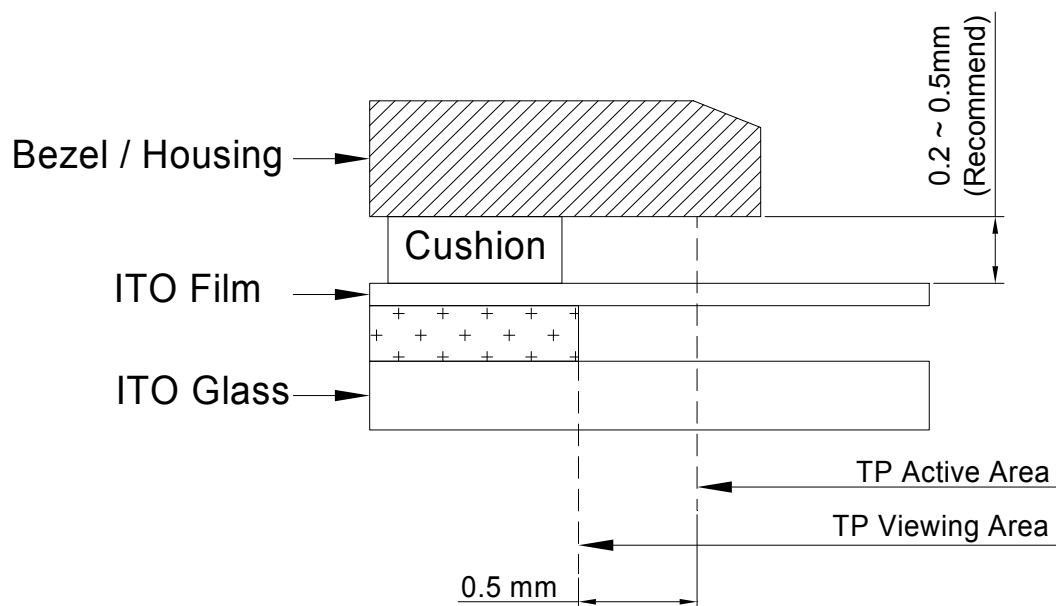
- (a) When mounting or dismounting the LCD modules, turn the power off.
- (b) Protect the LCD modules from electric shock.
- (c) The Driver IC control algorithms stated above should always obeyed to avoid damaging the LSI and electronic circuit.
- (d) Be careful to avoid mixing up the polarity of power supply for backlight.
- (e) Absolute maximum rating specified above has to be always kept in any case. Exceeding it may cause non-recoverable damage of electronic components or, nevertheless, burning.
- (f) When a static image is displayed for a long time, remnant image is likely to occur.
- (g) Be sure to avoid bending the FPC to an acute shape, it might break FPC.
- (h) Most of the touch screens have air vent to equalize the inside air pressure to the outside one. The air vent must be open and liquid contact must be avoided as the liquid may be

absorbed if the liquid is accumulated near the air vent.

- (i) For the fragility of ITO film, it should avoid to use too tapering pen as the input material.

### Touch Panel Mounting Notes

- (a) If a cushion is used between bezel/housing and film must be choose as free as enough to absorb the expansion and contraction to avoid the distortion of film.
- (b) The cushion must be placed out of the Viewing Area.
- (c) Bezel/Housing edge must be posited between Key Area and Viewing Area. The edge enters the Key Area may cause unexpected input if the gap is too narrow or foreign particles like dusts exist between Bezel/Housing and ITO film.
- (d) Mounting example:



The corner part has conductivity. Do not touch any metal part after mounting.

### Others

- a) If the liquid crystal leaks from the panel, it should be kept away from the eyes or mouth.
- b) For the fragility of polarizer, it is recommended to attach a transparent protective plate over the display surface.
- c) It is recommended to peel off the protection film on the polarizer slowly so that the electrostatic charge can be minimized.